Name	Mrs.Neha Abhijit Marathe
Designation	Assistant Professor
Department	Electronic and Telecommunication
Date of Joining the Institute	22/92/2010
Education	BE Electronics ME Microelectronics
Branch / Specialization	Microelectronics
Areas of Interest	Embedded Systems,Integrated Circuits,VLSI, Image Processing,Biomedical
Email	neha.sakhalkar@famt.ac.in

Academic Appointments

Name of the University / Institute/ Industry	Duration	Designation
Finolex Academy of Management and Technology, Ratnagiri	From 22/02/10 to till date	Assistant Professor

Total Experience – 13 Years

Awards / Honors / Prizes
1.Cracked GATE 2013(ECE) with 91.17 percentile
2.Got admission for ME on GATE score and received GATE Scholarship of Rs.12000 per month for 02 years

Papers Published

Papers Presented/Published in National / International Journals - 06

International Journals – 06

1.Published a Paper on **"Defense for Women Safety"** in International Journal of Innovative Research in Science,Engineering and Technology(IJIRSET) Vol 12,Issue 4,,April 2023

2.Published a paper **"IOT Based Driver Doziness System"** in International Journal of Creative Research Thoughts(IJCRT) .Vol 10,Issue 06, April 2022, ISSN : 2320-2882

3.Published a paper **"Biometric Attendance Monitoring System "** in International Journal of Scientific Research in Engineering and Management(IJSREM) Vol 04,Issue 04, April 2020,2582-3930

4.Published a paper "**Review paper on Vehicle Number late Recognition,"***International Journl of Engg Research and Technology,* vol. 8, no.4, pp. 282 - 286, 04-2019

5.Published paper ,**"CORDIC based hardware for Sine-Cosine Generation using VHDL"** in International Journal of Pure and Applied Research in Engineering and Technology - ISSN: 2319-5078

6.Published paper ," **Design and Implementation of CORDIC Hardware for Sine-Cosine Generation using VHDL"**, in International Journal of Advances in Management,Technology and Engineering Sciences - Vol. 4, Issue 6, March 2015, ISSN: 2249-7455

Papers Presented/Published in National/ International Conferences - 03

International Conferences – 03

1. Presented a paper **"IOT Based Driver Doziness System" in** International Conference on Communication and Computing 2022(ICCC-2022) held on 8-9 Apr,2022
2. Published paper ,"CORDIC based hardware for Sine-Cosine Generation using VHDL" in International Journal of Pure and Applied Research in Engineering and Technology - ISSN: 2319-5078

3.Published paper ," Design and Implementation of CORDIC Hardware for Sine-Cosine Generation using VHDL", in International Journal of Advances in Management,Technology and Engineering Sciences - Vol. 4, Issue 6, March 2015, ISSN: 2249-7455

Number of Workshops, Seminars, STTPs, FDPs Attended

Workshops

1.Attended **Workshop** on **Intellectual Property Rights** held in March, 2019 .

2.Attended One Week Workshop on **"Funding Schemes available for research proposals"**organized by RDCC of FAMT,Ratnagiri

3. Attended Two days ISTE workshop on '**Virtual Labs, Electronics** ' Conducted by IIT Bombay and MHRD initiative on 9th to 10th April 2011.

4.Attended a Two-Week ISTE workshop on '**Basic Electronics**' Conducted by IIT Bombay under the National Mission on Education through ICT (MHRD, Govt. of India) at Finolex Academy of Management & Technology, Ratnagiri from 28th June to 8th July,2011.

FDPs

1.Attended FDP on **AI Algorithms and Applications in Healthcare**, from 19th June to

23rd June 2023

2. Completed **FDP** on **Pedagogy for Online and Blended Teaching-Learning Process** held in September, 2017 .

3.Completed **FDP** on **Foundation Program for ICT in Education** held in August, 2017 .

STTPs

1. Completed **STTP** on **Digital Image Processing-Issues,Challenges and Research Advances** held in June, 2018 .

NPTEL

 1.Completed **NPTEL** course on **Design of Learner Centric MOOCS** held in July, 2019

2.Completed **NPTEL** course on **Introduction to Research** held in February, 2019 .

3.Completed **NPTEL** course on **Basic Electronics** held in January, 2018 .

Conferences and Seminars

1.Attended 3rd International Conference on Emerging Trends and Research in Engineering and Technology on 6th & 7th April 2015, IBSS College of Engineering, Amravati,Maharashtra

2.Attended International Conference on Latest Trends in Engineering and Technology in Global R&D Sectors on 27th & 28th March 2015 at Shree Rayeshwar Institute of Engineering and Information Technology,Shiroda,Goa

Professional Membership

1.Indian Society of Technical Education (I.S.T.E)

College Exam Duties

1.Worked as a Senior Supervisor of First Half of 2019 examination

2.Worked as a Senior Supervisor of 2nd Half of 2012 examination

3.Carried out exam related duties such as Moderator, Examiner and External Examiner for conducting theory, practical and oral exam as appointed by Board of Examination, Mumbai University.

4.Worked as Teacher in Charge for Practical's/Viva since 2011

5.Worked as a Member of Result checking committee and scrutinee committee

Other Experience

College Level Committees

1. Working as a Departmental Member of **Internal Complaint Committee**
2. Working as a Departmental Member of **Course Enrichment Committee**
3. Working as a Departmental Member of **Women Development Cell**
4. Working as a Departmental Member of **ISTE Students chapter**
5. Working as a department coordinator for **NAAC Criteria 3 committee**
7. Working as **NBA Criteria 5 Coordinator**
8. Working as a Departmental Member of **Academic Calendar Committee**
9. Worked as departmental organizing committee member in **2nd National Conference on "Recent Advances in Science and Technology"**
10. Worked as a departmental organizing committee member in **Two Days International Conference on Communication and Computing ICCC-2022**
11. Successfully Anchored the Inauguration Ceremony of **Two Days International Conference on Communication and Computing ICCC-2022**

Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester
1.	Integrated Circuits	T.E.EXTC SEM V
2.	Electronic Devices and Circuits	SE EXTC SEM III
3.	Linear Integrated Circuits	SE EXTC SEM IV
4.	Cyber Security and Laws	SE EXTC SEM VII
5.	Electronic Devices and Circuits Lab	SE EXTC SEM III
6.	Linear Integrated Circuits Lab	SE EXTC SEM IV
7.	Mini Project 1A Lab : PCB Design	SE EXTC SEM III
8.	Mini Project 2B Lab : FPGA	TE EXTC SEM VI
9.	Image Processing and Machine Vision	TE EXTC SEM VI

10.	VLSI Design	T.E.EXTC SEM VI
11.	Principle of Control Systems	S.E.EXTC SEM IV
12.	Circuit Theory and Networks	S.E.EXTC SEM III
13.	Digital Image Processing	B.E.EXTC SEM VIII
14.	Continuous Time Signals and Systems	T.E.ETRX SEM V
15.	Discrete Time Signals and Systems T	T.E. ETRX SEM VI
16.	Electrical Network Analysis an Systems	S.E.ETRX SEM III
17.	Digital System Design II	S.E.ETRX SEM VI
18.	Computer Organization	T.E. ETRX SEM VI