Name	Giri S. Kulkarni
Designation	Associate Professor
Department	Electronics and Telecommunication Engineering
Date of Joining the Institute	14-07-1997
Education	M.TECH (Micro Electronics) IIT Bombay
Branch / Specialization	Electronics & Communication
Areas of Interest	VLSI, Embedded Systems, Microprocessors and Microcontrollers
Email	giri.kulkarni@famt.ac.in

Academic Appointments

Name of the University / Institute/ Industry	Duration	Designation
Finolex Academy of Management and Technology, Ratnagiri	14/07/1997 to till date	Associate Professor
Maharashtra College of Engineering, Nilanga, Dist. Latur.	25/08/1993 to 30/06/1997	Lecturer
Total Experience – 30 Years		

Students' projects Guided at Undergraduate Level

AY 2022-23
i) Attendance Management System.
ii) IOT enabled Automatic Beverage Bottle filling.

AY 2021-22
i) IoT Based Smart Home Automation.
ii) IoT Based Weather Monitoring System.
iii) Smart Instructible Quadrupedal Robot for Avoiding Railway Tunnel Accidents.

AY 2020-21
i) Smart Bot to Handle Office.
ii) Smart Solar Grass Cutter.

AY 2019-20
i) Advanced Digital Locker System using Raspberry pi
ii) Smart Water Level Maintainer and Purity Indicator

AY 2018-19
i) Implementation of Multiprotocol Label Switching based Virtual Private Network.
ii) Fingerprint based Electronic Voting Machine.
iii) Wireless Communication using Li-Fi

Papers Published

Papers Presented/Published in National / International Journals - 04

1) **"Highly Robust Nanoscale Planar Double Gate MOSFET Device and RAM Cell Immune to Gate Misalignment and Process Variations"** in IEEE Explore IEEE Catalog Number: CFP0926C, ISBN: 978-1-4244-3832-7.

2) **"On the class of exact solutions of MHD fluid flow of second order type by creating forced oscillations on the porous boundary"** in **International Journal of Recent** Advances in Engineering & Technology IJRAET IRD India, vol. 4, no.8, pp. 1 - 8, 04-2016, ISSN (online) 2347-2812.

3) **"Partially Adaptive Cluster Based Routing for Network-on-Chip"** in Asian Journal of Convergence in Technology, vol. 1, no.2, pp. 1 - 6, 3-2016.

4) **"Advance Digital Locker System Using Raspberry Pi"** in International Journal of Scientific Research in Engineering and Management (IJSREM) Volume :04 Issue:04, April 2020. ISSN 2582-3930

Papers Presented/Published in National/ International Conferences - 03

1) "Highly Robust Nanoscale Planar Double Gate MOSFET Device and RAM Cell Immune to Gate Misalignment and Process Variations" in 2nd IEEE workshop on Electronic Devices and Semiconductor Technology (IEDST 2009) held at IIT, Bombay in June 1-2, 2009
Details of Papers presented at National Conferences.

2) "On the class of exact solutions of MHD fluid flow of second order type by creating forced oscillations on the porous boundary" Proceedings of International Conference on Recent Trends in Computing, Communication and Science (ICRTCCS) held on 8-9 Jan 2016 at International Institute of Information Technology, Pune. ISBN 978-2-642-24819-9.

3) "Partially Adaptive Cluster Based Routing for Network-on-Chip" in 2nd National Conference on Recent Advances in Science and Technology,2K16 (RAST-2k16), 17-18 Feb 2016 at FAMT, Ratnagiri. ISBN. 978-93-83046-31-7.

Number of Workshops, Seminars, STTPs, FDPs Attended – 23

1) **"Linux for Developers"** Course authorized by Linux Foundation and offered through Coursera, completed on ctober13, 2021.

2) **"Embedded Systems – An Application driven Approach"** FDP organized by AICTE, ATAL Academy, ST Microelectronics and ARM Education from August 25th – 27th 2021.

3) **"VLSI Design from Basics to Advance"** One-week STTP organized by KC College, IETE in association with CADENCE Design System and NXP Solutions from 25th Jan to 30th Jann2021.

4) **"Artificial intelligence in Telecommunication"** Three-day FDP program organized by Dept of Electronics and Telecommunications, FAMT Ratnagiri, from 28th to 30th June 2021.

5) **"Introduction to Latex"**, Six-day FDP Conducted by Xavier institute of Engineering, Mumbai from 15th to 20th June 2020.

6) **"Exploring the dimensions of Innovation, Incubation and Engineering technologies to embrace post covid changes"** ONE Week FDP organized by Dept of Mechanical Engineering, FAMT, Ratnagiri, from 8th June to 12th June 2020.

7) **"Moodle Learning Management System"** ONE Week FDP organized by PICT Pune in association with IIT Bombay during 26th May to 30 May 2020.

8) **"Embedded Software and Hardware Architecture"** course authorized by University of Colorado and offered through Coursera, completed on 25th May 2020.

9) **"Outcome Based Education: A Step towards Excellence"** One Week FDP organized by Govt. College of Engineering, Karad under Margadrshan Scheme of AICTE from 11-15 May 2020.

10) **"Embedded System Design with ARM"** eight Week NPTEL Course Jan - Mar 2020.

11) **"NBA Preparation"** Two-day FDP program organized by Dept of E and TC, FAMT, Ratnagiri from 5-6 March 2020.

12) **"Python Programming and Raspberry PI"** organized by Dept of E and TC, FAMT, Ratnagiri from 28-31 July 2019.

13) **"Open-Source Solutions in Technology-Learning Process"** AICTE-ISTE approved induction/Refresher program organized by FAMT, Ratnagiri from 24-28 June 2019.

13) **"Microprocessors and Microcontrollers"**, Twelve-week NPTEL course Jan- Apr 2018 with Elite category certification.

14) **"Network Associate – Routing and Switching"**, Training program conducted by TeleNetworks Technologies in Feb 2017.

15) **"Use of ICT in Education for Online and Blended Learning"**, FDP conducted by IIT Bombay from 2 May-10 July 2016.

16) "**Mobile Antennas-Fundamentals, Design, Fabrication and Testing"**, Two Day workshop conducted by Department of E and TC and Electronics of FAMT on 01-02 August 2015.

17) **"Introduction to Research Methodologies",** workshop Conducted by IIT Bombay under the National Mission on Education through ICT from 25/06/2012 to 04/07/2012.

18) **"Virtual Labs, Electronics",** workshop conducted by Electrical Department, IIT Bombay on April 09 and 10, 2011.

19) **"Simulation of Electronic Circuits & Power Electronic Systems using SEQUEL",** Two-day workshop Organized by Department of E and TC, FAMT on October 31st and 1st November, 2009.

20) **"VLSI Design and Technologies",** Three-Day workshop conducted by Shaha & Anchor Kutchhi Engineering College from 12 to 14 August 2004.

21) **"Advances in Micro Electronics",** workshop conducted by IIT Bombay and FAMT on 15th March 2003.

22) **"Design of VLSI systems and its applications",** Three-Day workshop conducted by ISS College of IT and Engineering for Women from February 16 to 18, 2001.

23) **"Reprogramming of Computer Education in Engineering/Polytechnics",** Two-Day Seminar organized by SGGS Nanded and C-DAC Pune at C-DAC Pune on August 10-11,1996.

Professional Membership

1. Indian Society for Technical Education (**ISTE): Life member Membership Id :LM 29422 since 2000.**

2. Institution of Electronics and Telecommunication Engineers (IETE): Associate member: Membership No. AM-503393 since November 2022.

Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester
1.	Microcontrollers	2 / E and TC / IV (R-2019)
2.	Digital VLSI Design	3 / E and TC / V (R-2019)
3.	Microprocessors and Peripheral Interfacing.	3 / E and TC / V (R-2016)
4.	Microcontrollers and Applications	3/ E and TC / VI (R-2016)
5.	VLSI Design	3 / E and TC / VI (R-2016)
6.	Basics of VLSI	4 / Electronics/ VII

7.	Advanced VLSI	4/ Electronics/ VIII
8.	Digital Design -I	2/ E and TC / III
9.	Digital Design -II	2/ E and TC / IV
10.	Designing with Integrated Circuits	5/ Electronics/ V
11.	Logic Circuits	4 / Electronics/ IV
12.	Electronic System Design	4 / Electronics / VIII