Name	Miss. Gauri Suresh Bhosale	
Designation	Assistant Professor	
Department	EXTC	
Date of Joining the Institute	02/01/2023	
Education	B.E. Electronics, FAMT, 2008 M.E. Electronics, KJSCE, 2015	
Branch / Specialization	Electronics Engineering	
Areas of Interest	Programming in C, VHDL, Verilog	
Email	gauri.bhosale@famt.ac.in	

Academic Appointments

Name of the University / Institute/ Industry	Duration	Designation
Finolex Academy of Management and Technology, Ratnagiri	From 2/01/23 to till date	Assistant Professor

Total Experience – ____Years
9.5 years

Students projects Guided at Undergraduate Level
Smart Mirror

Papers Published

Papers Presented/Published in National / International Journals

International Journals – 01

Paper titled "Asynchronous Router for Network-on-chip on FPGA" in International Journal of Computer Applications (0975 – 8887)

Papers Presented/Published in National/ International Conferences

International Conferences – 01

Paper presented entitled "Asynchronous Router for Network-on-chip on FPGA" in International Conference on Computer Technology (ICCT 2015)

Number of Workshops, Seminars, STTPs, FDPs Attended - 1

STTPs and FDPs

Completed **ISTE Approved One Week Online FDP on AI Algorithms and Applications in Healthcare** organized by Department of Electronics and Telecommunication Engineering, from 19th June to 23rd June2023

Other Experience

College Level Committees

Working as a time-table coordinator, Major Project coordinator and Mentorship coordinator

Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester
1.	Digital Logic Design	SE/EXTX/Sem III
2.	Signals and systems	SE/EXTX/Sem IV
3.	Microprocessors and Microcontroller	SE/IT/Sem III and IV
4.	Linear Integrated Circuits	TE/ETRX
5.	Modern Television Engineering	TE/EXTC