

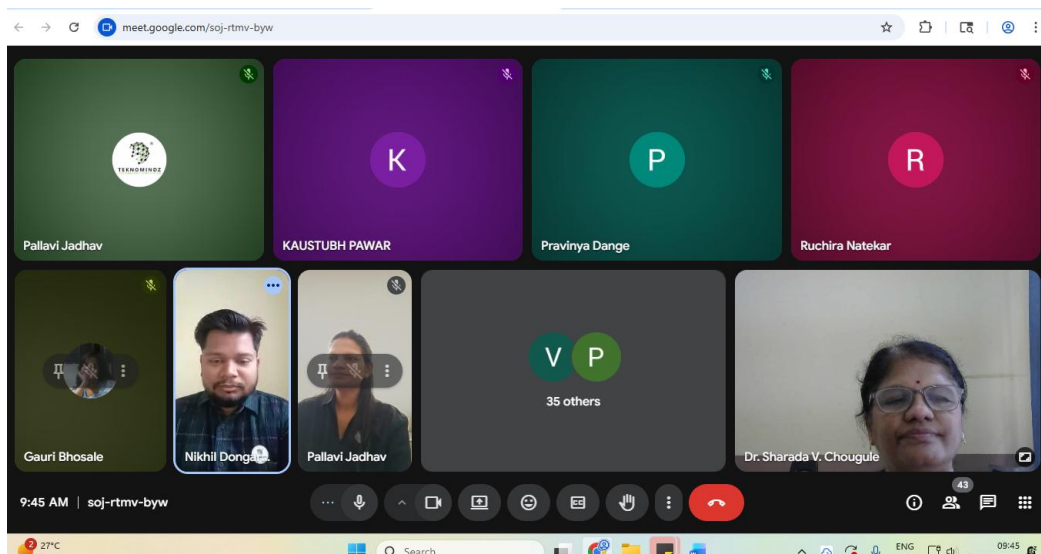
Hands-on PCB Designing Workshop

Department of Electronics and Telecommunication Engineering organized a **Hands-on PCB Designing Workshop** on 18th March 2026 for second-year EXTC students. This workshop was conducted under IETE ISF and under an MoU with Teknomindz Institute of Advanced Technology, Pune.

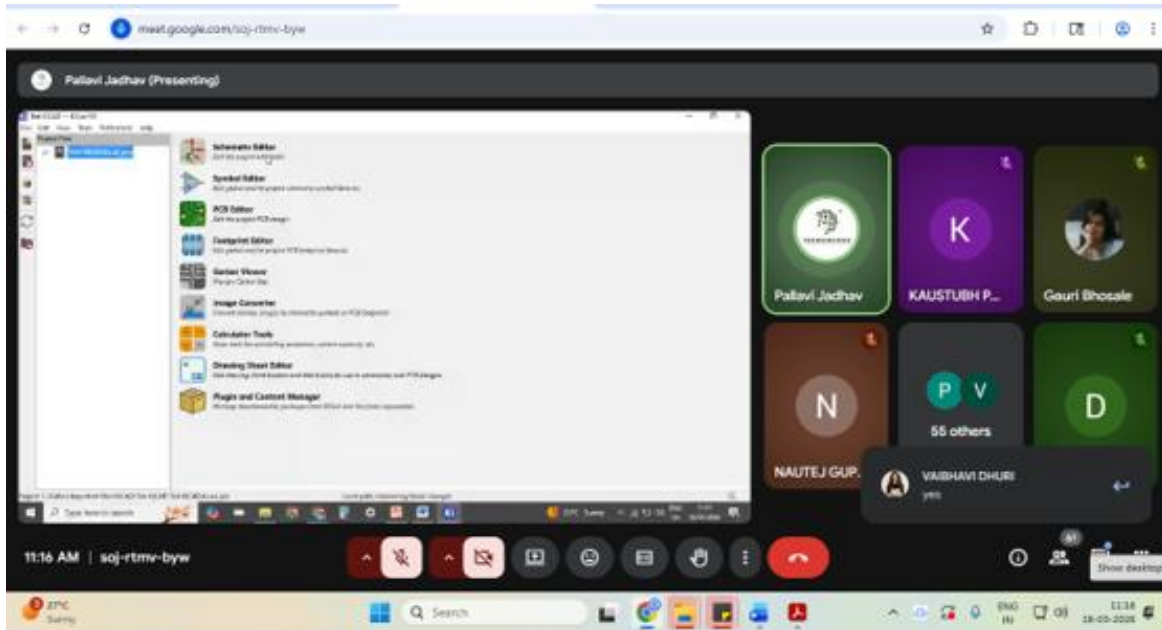
Ms. Pallavi Dongardive, Senior Instructor and PCB Designer, Teknomindz Institute of Advanced Technology, Pune, was the resource person for the workshop. Ms Pallavi conducted a workshop covering schematic design, simulation for result verification, PCB layout generation, and the creation of Gerber files for both single-layer and double-layer PCBs. The objectives of the workshop were:

- To introduce students to the fundamentals of PCB design, including schematic capture, circuit simulation, and layout design using KiCad.
- To provide hands-on experience in PCB designing by applying design rules and generating Gerber files for fabrication.
- To develop practical skills through project-oriented PCB design case studies, enhancing problem-solving and real-world application abilities.

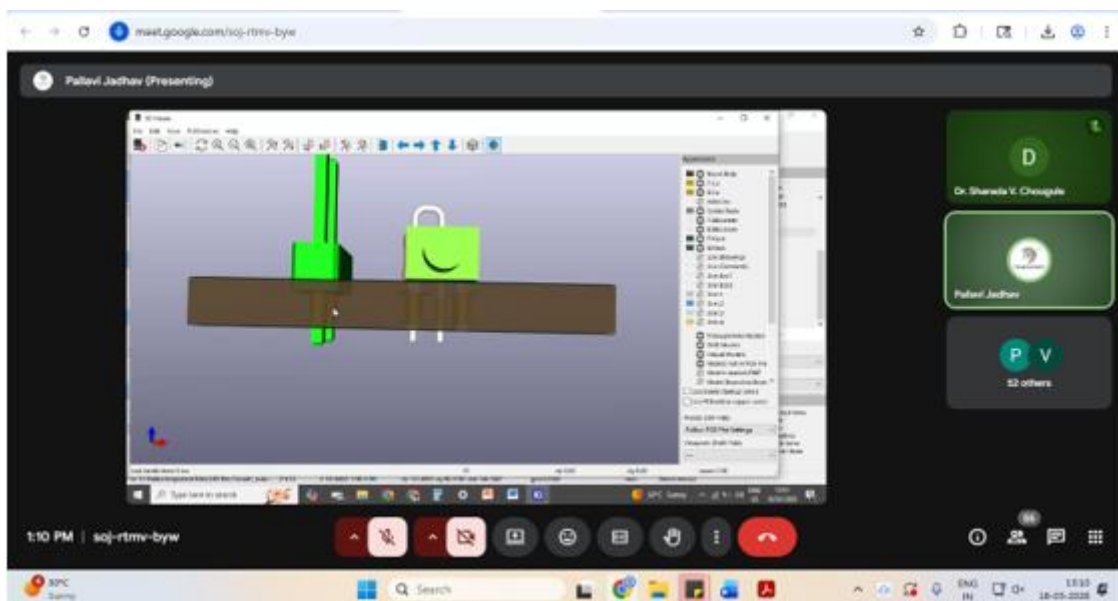
The session was coordinated by **Miss. G. S. Bhosale**, Assistant Professor of the EXTC Department, under the guidance of HOD EXTC **Dr. S.V. Chougule**. A total of 56 Students of SE EXTC attended the workshop and discussed their doubts. Students shared their feedback on this workshop.



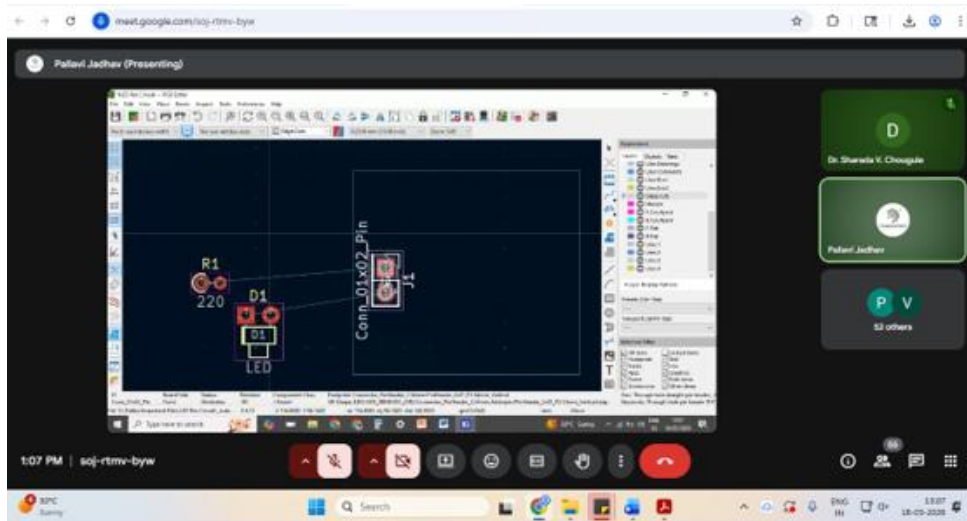
Dr. Sharada V. Chougule, HOD EXTC, gave a brief introduction about the resource person and the workshop



KiCad software for generating schematic and Gerber file



3D view of PCB layout



Component placing while avoiding overlapping of tracks



The students doing hands-on practice in labs simultaneously with the demonstration conducted by Ms. Pallavi