

FINOLEX ACADEMY OF MANAGEMENT AND TECHNOLOGY, RATNAGIRI
DEPARTMENT OF ELECTRONICS ENGINEERING
TIMETABLE FOR 2nd HALF OF 2018

CLASS: TE			w.e.f. July 2nd , 2018			CLASSROOM: EN-1/5				
	9.15-10.15	10.15-11.15	11.15-11.30	11.30-12.30	12.30-1.30	01.30-2.15	2.15-3.15	3.15-4.15	4.15-5.15	
MON	DLIC (MAJ)	DC (RPT)		MCA B1 (SRN)		L U N C H	DCS (RBI)	EME (GGB)	Gate Coaching	
				DC B2 (RPT)						
TUE	DLIC (MAJ)	MCA (SRN)		DC B1 (RPT)				EME (GGB)	DCS (RBI)	BCE (PQR)
				DCS B2 (RBI)						
WED	EME (GGB)	MCA (SRN)		DCS B1 (RBI)				BCE (PQR)	DC (RPT)	EME-T(GGB)
				DLIC B2 (MAJ)						
THURS	EME (GGB)	DC (RPT)		DLIC B1 (MAJ)				DCS (RBI)	DLIC (MAJ)	Gate Coaching
				BCE B2 (DMB)						
FRI	DLIC (MAJ)	DC (RPT)		BCE B1 (PQR)				DCS (RBI)	MCA (SRN)	T & P Activity
				MCA B2 (SRN)						
SAT	Course Enrichment/ Remedial			MCA (SRN)	MCA (SRN)					

HoD Electronics

Dean Academics