

FINOLEX ACADEMY OF MANAGEMENT & TECHNOLOGY, RATNAGIRI.

DEPARTMENT OF ELECTRONICS ENGINEERING.

JAN 2018 TO APRIL 2018

TIME-TABLE (SEM - VI)

T.E. (ETRX)

LECTURE HALL- (EN 1/5)

w.e.f. 02 January 2018

TIME	09.15-10.15	10.15-11.15	11.15-11.30	11.30-12.30	12.30-01.30	01.30-2.15	02.15-3.15	03.15-04.15	04.15-05.15	
MONDAY	VLSI D / DSPP / AI&PE VVN / RPT / (RBI & ARS)		BREAK	PE- I ARS	DSPP RPT	LUNCH	AIS RBI	BVLSI /VVN	GATE COACHING	
TUESDAY	DSPP / AI&PE / MINI PROJ. RPT / (RBI & ARS) / SRN			AIS RBI	PE- I ARS		CO SAS	BVLSI /VVN	GATE COACHING	
WEDNESDAY	AI&PE / MINI PROJ. / VLSI D (RBI & ARS) / SAS / VVN			CO SAS	MITM AGA		DSPP RPT	PE- I ARS	COURSE ENRICHMENT	
THURSDAY	MINI PROJ. / VLSI D / DSPP SRN / VVN / RPT			BVLSI /VVN	AIS RBI		CO SAS	PE- I ARS	COURSE ENRICHMENT	
FRIDAY	BVLSI /VVN	MITM AGA		AIS RBI	DSPP RPT		COURSE ENRICHMENT			
SATURDAY	DSPP RPT	DSPP RPT		CO SAS	CO SAS		T & P ACTIVITY			

SUBJECT	TEACHER	LAB
BVLSI	/ V. V. NIMBALKAR	VLSI LAB
AIS	R. B. INGALE	INSTRUMENTATION LAB
CO	S. A. SHETE	--
PE-I	A. R. SUTAR	INSTRUMENTATION LAB

SUBJECT	TEACHER	LAB
DSP & P	R. P. TIVAREKAR	VLSI LAB
MITM	A. G. APTE	--
MINI PROJECT	S. R. NALAGE & S. A. SHETE	EDC / ADIC LAB

PREPARED BY R. P. TIVAREKAR
A. G. APTE

HOD