

FINOLEX ACADEMY OF MANAGEMENT AND TECHNOLOGY, RATNAGIRI
DEPARTMENT OF ELECTRONICS & TELECOMMUNICATION ENGINEERING.

JAN 2019 - MAY 2019

TIME TABLE

CLASS: T. E. EXTC

SEM VI

LECTURE HALL-2/11

CLASS TEACHER: PROF. S. D. MAINKAR

W.E.F: 2nd January, 2019


TIME	9.15-10.15	10.15-11.15	11.15-11.30	11.30-12.30	12.30-01.30	1.30-2.15	02.15-03.15	03.15-4.15	04.15-5.15	
MON	CCN	IPMV	BREAK	VLSI	MCA	LUNCH	ARWP	MCA/LIB/VLSI /IPMV LAB 2/—/LAB 1/LAB 3		
TUE	CCN	IPMV		ARWP	MCA		VLSI	ARWP/MCA/LIB/VLSI LAB 3/LAB 2/—/LAB 1		
WED	VLSI	ARWP		CCN	IPMV		MCA	IPMV/ARWP/—/CCN LAB 1/LAB 2/—/LAB 3		
THUR	CCN/IPMV/MCA/LIB LAB 2/LAB 1/LAB 3/—			VLSI	ARWP		IPMV	LIB/VLSI /IPMV/ARWP LAB 1/LAB 2/LAB 3		
FRI	VLSI /CCN/ARWP/LIB LAB 1/LAB 2/LAB 3/—			MCA	CCN		LIB/LIB/CCN/MCA LAB 2/LAB 3			
SAT	NPTEL/COURSE ENRICHMENT/GATE COACHING									


SUBJECT	TEACHER
MCA & MCA LAB	PROF. G. S. KULKARNI
VLSI & VLSI LAB	/PROF. R. R. KULKARNI
IPMV LAB	/PROF. N. S. SAKHALKAR (BATCH T3 & T4)
IPMV LAB	/DR. S. V. CHOUGULE (BATCH 1 & 2)


SUBJECT	TEACHER
ARWP & ARWP LAB	PROF. S. D. MAINKAR
IPMV	/DR. S. V. CHOUGULE
CCN & CCN LAB	PROF. S. S. ATHALYE

LAB 1	SIM LAB EN 2/1
LAB 2	DSP LAB EN 2/2
LAB 3	DSP LAB EN 2/2

PREPARED BY-
/M. S. KOLWANKAR
V. D. CHAVAN


Class Teacher
PROF. S. D. MAINKAR


HOD
Dr. S. V. Chougule


Dean Academics
Dr. M. S. Kirkire