

FINOLEX ACADEMY OF MANAGEMENT & TECHNOLOGY, RATNAGIRI.

DEPARTMENT OF ELECTRONICS ENGINEERING.

JAN 2017 TO APRIL 2017

TIME-TABLE (Vth SEM)

T.E.(ETRX.)

LECTURE HALL-(EN 1/5)

w.e.f. 05/ 01/ 2017

TIME	09.15-10.15	10.15-11.15	11.15-11.30	11.30-12.30	12.30-01.30	01.30-2.15	02.15-3.15	03.15-04.15	04.15-05.15
MONDAY	VLSIDL/AIS & PEL/MP II/DSP & PL /VVN,(MSP & ARS),GGB,SAS			AIS MSP	BVLSI /VVN		PE-I ARS	CO /GDK	
TUESDAY	DSP & PL/VLSIDL/AIS & PEL/MP II SAS,/VVN,(MSP & ARS),GGB			AIS MSP	BVLSI /VVN		CO /GDK	DSP & P SAS	
WEDNESDAY	MP II/DSP & PL/VLSIDL/AIS & PEL ABV,SAS,/VVN,(MSP & ARS)			MITM ARP	PE-I ARS		CO /GDK	DSP & P SAS	
THURSDAY	AIS & PEL/MP II/DSP & PL/VLSIDL (MSP & ARS),MAJ,SAS,/VVN			PE-I ARS	DSP & P SAS		AIS MSP	BVLSI /VVN	
FRIDAY	BVLSI /VVN	CO /GDK		PE-I ARS	AIS MSP		MITM ARP	T & P ACTIVITY/ COURSE ENRICHMENT/GATE	
SATURDAY	DSP & P SAS	DSP & P SAS		LIB	LIB				

SUBJECT	TEACHER	LAB
BVLSI	PROF./V.V.NIMBALKAR	VLSI LAB
PE-I	PROF.A.R.SUTAR	INSTRU.LAB
DSP & P	PROF.S.A.SHETE	VLSI LAB
AIS	PROF.M.S.PATIL	INSTRU.LAB

SUBJECT	TEACHER	LAB
CO	PROF./G.D.KHEDEKAR	
MITM	PROF.A.R.PALVANKAR	
MP-II		EDC.LAB

PREPARED BY PROF.A.B.VARTAK
 PROF.M.S.PATIL

HOD

PROF.G.G.BHIDE