

FINOLEX ACADEMY OF MANAGEMENT AND TECHNOLOGY, RATNAGIRI
DEPARTMENT OF ELECTRONICS ENGINEERING
TIMETABLE FOR 2nd HALF OF 2018

CLASS: BE			w.e.f. July 2nd , 2018				CLASSROOM: EN-1/9		
	9.15-10.15	10.15-11.15	11.15-11.30	11.30-12.30	12.30-1.30	01.30-2.15	2.15-3.15	3.15-4.15	4.15-5.15
MON	CCN (ABV)	DIP (XYZ)/ OFC (GGB)		ESD (MAJ)	ICT (VVN)	L U N C H	ESD B1 (MAJ)		
							ICT B2 (VVN)		
							DIP B3 (ABV)		
TUE	DIP (XYZ)/ OFC (GGB)	ICT (VVN)		ESD (MAJ)	PE-II (ARS)		ICT B1 (VVN)		
							ESD B2 (MAJ)		
							CCN B3 (ABV)		
WED	ESD (MAJ)	CCN (ABV)		PE-II (ARS)	DIP (XYZ)/ OFC (GGB)		PE-II B1 (ARS)		
							CCN B2 (ABV)		
							ESD B3 (VVN)		
THURS	PE-II (ARS)	CCN (ABV)		ICT (VVN)	DIP (XYZ)/ OFC (GGB)		CCN B1 (ABV)		
					PE-II B2 (ARS)				
					ICT B3 (VVN)				
FRI	PE-II (ARS)	CCN (ABV)	ESD (MAJ)	ICT (VVN)	OFC B1 (GGB)				
					DIP B2 (XYZ)				
					PE-II B3 (ARS)				
SAT	Project -I			Project -I			Project -I		

HoD Electronics

Dean Academics