

**FINOLEX ACADEMY OF MANAGEMENT & TECHNOLOGY,RATNAGIRI.
DEPARTMENT OF ELECTRONICS ENGINEERING.**

JUL. 2017 TO NOV. 2017

TIME-TABLE

B.E.(ETRX.)

LECTURE HALL: (EN- 1/9)

w.e.f. : 3rd July 2017

TIME	09.15-10.15	10.15-11.15	11.15-11.30	11.30-12.30	12.30-01.30	01.30-2.15	02.15-3.15	03.15-04.15	04.15-5.15
MONDAY	ICT /VVN	CCN ABV	BREAK	ESD MAJ	PE-II ARS	LUNCH	DIP /OFC SAS , AGA	ESD / ICT / PE-II/ CCN MAJ,/VVN, ARS,ABV	
TUESDAY	CCN ABV	ICT /VVN		ESD MAJ	PE-II ARS		ICT/PE-II/CCN/ELEC-I /VVN,ARS,ABV,SAS+AGA		
WEDNESDAY	CCN ABV	DIP /OFC SAS , AGA		ESD MAJ	ICT /VVN		PE-II/ CCN / ELEC-I/ESD ARS, ABV, SAS+AGA,/MAJ		
THURSDAY	CCN ABV	PE-II ARS		ICT /VVN	DIP /OFC SAS , AGA		CCN / ELEC-I/ ESD/ICT ABV, SAS+AGA, MAJ,/VVN		
FRIDAY	PE-II ARS	ESD MAJ		ELEC-I/ESD/ICT/PE-II SAS+AGA,MAJ,/VVN/ARS			DIP /OFC SAS , AGA	T & P ACTIVITY	
SATURDAY	PROJECT-A								

SUBJECT	TEACHER	LAB
ICT	PROF./V.V.NIMBALKAR	VLSI LAB
PE-II	PROF.A.R.SUTAR	INSTR LAB
DIPD	PROF.S.A.SHETE	CC LAB
CCN	PROF.R.B.VARTAK	VLSI LAB

SUBJECT	TEACHER	LAB
OFC	PROF.A.G.APTE	COMM.LAB
ESD	PROF.M A.JADHAV	MPMC LAB

PREPARED BY: Prof.R.B.Ingle, Prof.A.B.Vartak

HOD

Prof.G.G.BHIDE