

Name	SUDHIR S. WAMANE B.E. Electrical Engineering M.TECH (Control System)
Date of Birth	05/06/1987
Highest Qualification	Master in Technology
Branch / Specialization	Electrical Engineering (Control Systems)
Designation	Assistant Professor
Department	Electrical Engineering
Date of Joining the Institute	22/07/2008



Work Experience	<ul style="list-style-type: none"> Working with Finolex academy of Management and Technology, Ratnagiri as an Assistant Professor from 22/07/2008 to till date (9 years experience)
------------------------	--

Number of Papers Presented/Published in National/ International Conferences	02
--	-----------

Details

1. "Performance based comparison of UPOC compensating signal generation algorithms under distorted supply and non linear load conditions" , 8th IEEE Conference on Industrial Electronics and Applications , Melbourne, Australia, 2013, ISBN: 978-1-4673-6320-4 Pages 38-42.
2. "Study on Mitigating Inrush Current of Transformer using Point on Wave Switching and Prefluxing Technique", 2nd National level conference on Recent Advances in Science and Technology, Ratnagiri, India, 2016, ISBN: 978-93-83046-31-7 Pages 94-98.

Number of Papers Presented/Published in National/ International Journals	02
---	-----------

Details

1. "A comparative study on compensating current generation algorithms for Shunt active filter under Non-linear load conditions", International Journal of Scientific and Research Publications (IJSRP), June 2013, Vol. 3, Issue 6, ISSN 2250-3153.
2. "Experimental study on helical coil heat exchanger", International Journal of Scientific and Research Publications (IJSRP), May 2017, Vol. 7, Issue 5, ISSN 2250-3153.

Number of Workshops, STTPs Attended

12

Details

1. Attended Two week ISTE STTP on "Electrical Power System" conducted by IIT, Kharagpur, during 12th June– 15th July. 2017.
2. Attended One week workshop on "FACTS Devices and distributed generation through ICT" conducted by NITTTR, Chandigarh, during 6th – 10th Feb. 2017.
3. Attended two days workshop on "Raspberry pi" organized by FAMT, Ratnagiri, during 10th -11th Feb. 2017.
4. Attended one day workshop on "Inter collegiate Research convention Avishkar 2016" organized by VPM's MPCOE, Velneshwar, on 24th Sept. 2016.
5. Attended Two-week ISTE workshop on "Pedagogy for Effective use of ICT in Engineering Education" conducted by IIT Bombay under the national mission on Education through ICT (MHRD, Govt. of India), during 5th -31st Jan 2015.
6. Attended Two-week ISTE workshop on "Signals and Systems" conducted by IIT Kharagpur under the national mission on Education through ICT (MHRD, Govt. of India), during 2nd -12th Jan 2014.
7. Coordinated Two-week ISTE workshop on "Control System" conducted by IIT Kharagpur under the national mission on Education through ICT (MHRD, Govt. of India), during 2nd -12th Dec 2014.
8. Attended One-week Coordinators workshop on "Control System" conducted by IIT Kharagpur under the national mission on Education through ICT (MHRD, Govt. of India), during 15th - 19th Sept 2014.
9. Attended Two days workshop on "Nonlinear Dynamical Systems: Modelling and Analysis" at V.J.T.I., Mumbai, during 27th - 28th Jan 2013.
10. Attended One day workshop on "Quality Education in Engineering and Technical Institution" organized by FAMT, Ratnagiri under the FAMT ISTE Chapter on 24th Jan 2009.
11. Attended One day workshop on "Entrepreneurship Awareness Programme" conducted by MITCON Consultancy Services Ltd. in association with FAMT, Ratnagiri (Sponsored by Department of science and Technology, New Delhi).
12. Attended Two day workshop on "Simulation of Electronic Circuits and Power Electronics System using SEQUEL" conducted by FAMT, Ratnagiri, during 31st Oct & 1st Nov 2009.

Students projects Guided at Masters Level

NIL

Membership in Professional Bodies

01

Details

1. **Indian Society of Technical Education (I.S.T.E)**

**Seminars/Lecture/
Invited Talks
Delivered As
Resource Person
Details**

12

1. Expert Lecture on "Control System Design" at Sandip foundation engineering college, Nashik.
2. Conducted One day workshop on "PROTEUS Software" at FAMT, Ratnagiri on 10th Sept. 2015.
3. Conducted Add-on course on "MATLAB Software" at FAMT, Ratnagiri on 23rd Jan. 2016.
4. Conducted course enrichment on "Basics of FACTS devices via MATLAB Software" at FAMT, Ratnagiri on 8th Feb. 2016.
5. Conducted Two days workshop on "Analog and Digital Circuits Design using PROTEUS Software" at FAMT, Ratnagiri during 19th – 20th March 2016.
6. Conducted Add-on course on "MATLAB Software (Simulink)" at FAMT, Ratnagiri on 24th Jan. 2017.
7. Conducted Two days workshop on "Analog and Digital Circuits Design using PROTEUS Software" at FAMT, Ratnagiri during 4th – 5th March 2017.
8. Conducted course enrichment on "Basics of FACTS devices via MATLAB Software (Simulink)" at FAMT, Ratnagiri on 15th March 2017.
9. Expert Lecture on "FACTS Devices" at Rajaram Shinde College of engineering, Pedhambe on 6th April 2017.
10. Conducted One day workshop on "Analog and Digital Circuits Design using PROTEUS Software" at RSCOE, Pedhambe on 6th April 2017.
11. Conducted Course Enrichment on "Tuning of PID Controllers" at FAMT, Ratnagiri on 12th August 2017. Expert
12. Expert Lecture on "Control Systems" at Rajaram Shinde College of engineering, Pedhambe on 23th Sept. 2017.

**Awards / Honors /
Prizes**

02

Details

1. Secured 2nd rank in Mumbai University in Final Year of Electrical Engineering Degree (B.E.) (2007-2008).
2. Awarded by "Late Smt. Mohini P. Chhabria Award" For standing First in B.E. Electrical at FAMT college in 2008.

**Research /
Sponsored /
Consultancy Projects**

NIL