

**Name** MR. Ashwin B. Chettri  
**B.E.(E&TC), M.Tech(Electronics Technology)**

**Designation** Assistant Professor

**Department** EXTC

**Date of Joining the Institute** 06/01/2014

**Education**  
 B.Tech, E&TC IETE, 2010  
 M.Tech, Electronics Technology, 2014

**Branch / Specialization** Electronics Technology

**Areas of Interest** Electronics Hardware, Embedded system design

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### Academic Appointments

Name of the University / Institute / Industry	Duration	Designation
<b>Finolex Academy of Management and Technology, Ratnagiri</b>	<b>From 06/01/14 to till date</b>	<b>Assistant Professor</b>

**Total Experience – 5 Years**

### Awards / Honors / Prizes

- GATE qualified 2011 and hence received a scholarship of**
- NPTEL Course completed with ELITE medal**

### Seminars/Lecture/Invited Talks Conducted

- Conducted lecture **on "INDUSTRIAL ELECTRONICS" held on 13<sup>th</sup> DEC 2017** as a guest lecturer at Govindrao Nikam madhyamik and higher secondary school, Sawarde, Ratnagiri for 11 and 12 electronics students
- Conducted workshop **on "PCB Designing" held on July 2017** as a resource person and coordinator at Finolex Academy of Management and Technology Ratnagiri organized by Department of ENTC

## Papers Published

### Papers Presented/Published in National / International Journals - 02

#### International Journals – 02

1. Paper titled **“Simple and Low Cost IC Tester for Analog and Digital Labs”** published in ASIAN JOURNAL OF CONVERGENCE IN TECHNOLOGY (AJCT ISSN : 2350 - 1146).
2. Paper titled **“Improving Speed and Security of EVM’s using Finger Print and Face Recognition”** published in **IJERT** (ISSN : **2278-0181**).

### Papers Presented/Published in National/ International Conferences - 08

#### International Conferences – 02

1. Paper presented titled **“Improving Speed and Security of EVM’s using Finger Print and Face Recognition”** in Interdisciplinary One day International Conference on **‘Technical and Innovative Advancement in Computing and Communication Engineering’** organized by Marudhar Engineering College, Bikaner April 2013

#### National Conferences – 02

1. Paper presented titled **“Simple and Low Cost IC Tester for Analog and Digital Labs”** at 2<sup>nd</sup> National Conference on Recent Advances in Science and Technology (RAST) organized by Finolex Academy of Management and Technology, Ratnagiri on 17<sup>th</sup> and 18<sup>th</sup> February 2016.

Paper Published in Proceeding of **Conference ISBN: 978-93-83046-31-7**, pp. 188-192

### Number of Workshops, Seminars, STTPs, FDPs Attended – 31

#### STTPs and FDPs

1. Completed Online course on **‘Basic Electronics’** offered by IITBombayX, an online learning initiative of NPTEL **Indian Institute of Technology**. This online course was conducted between JAN 2017 - April 2017

#### Conferences and Seminars

1. Attended 2<sup>nd</sup> **National Conference on Recent Advances in Science and Technology (RAST)** organized by Finolex Academy of Management and Technology, Ratnagiri from 17<sup>th</sup> to 18<sup>th</sup> February 2016

### Two Week or One Week Workshops

1. Attended **One Week Workshop** on **"Funding Schemes Available for Research Proposals"** Organized by Research Development and Consultancy cell (RDCC) of Finolex Academy of Management and Technology, Ratnagiri from 13<sup>th</sup> February to 23<sup>rd</sup> April 2016
2. Attended **Two Week ISTE workshop** on **'Computer Networking'** Conducted by IIT Bombay under the National Mission on Education through ICT from 28<sup>th</sup> to 5<sup>th</sup> July 2014
3. Attended **Two Week ISTE workshop** on **'Computer Programming'** Conducted by IIT Bombay under the National Mission on Education through ICT from 20<sup>th</sup> MAY to 21<sup>th</sup> July 2014

### Professional Membership

1. Lifetime Member of Institution of electronics and telecommunication engineers (I.E.T.E), New Delhi. Membership number: 184952

### College Exam Duties

1. Worked as an **Assistant CAP coordinator** for the 2015 paper assessment at **FAMT CAP**.
2. Carried out exam related duties such as **junior supervisor, Scrutiny officer, University Examiner and External Examiner** for conducting theory, practical and oral exam as appointed by Board of Examination, Mumbai University.

### Other Experience

#### College Level Committees

1. Working as a **Departmental Academic calendar coordinator** of **Academic Calendar committee** of Finolex Academy of Management and Technology, Ratnagiri.
2. Working as a **Member** of FAMT magazine Committee
3. Worked as departmental organizing **committee member** in 2nd National Conference on **"Recent Advances in Science and Technology"**
4. Working as a member for **NAAC Criteria VI committee**
5. Working as **Teacher in charge** of **ASENT (Students association of EXTC)**

### **Judge Panel / Staff Selection Committee / Subject Expert**

1. Worked as a **Judge** for **Brainwaves 2k15 A National Level Technical Paper Presentation competition** organized by Finolex Academy of Management and Technology, Ratnagiri
2. Worked as a **Judge** for **Brainwaves 2k15 A National Level Project competition** organized by Finolex Academy of Management and Technology, Ratnagiri
3. Worked as a **Judge** for **Brainwaves 2k16 A National Level Technical Paper Presentation competition** organized by Finolex Academy of Management and Technology, Ratnagiri
4. Worked as a **Judge** for **TechSpring 2k16 A Technical Paper Presentation competition** organized by ASENT, Electronics & Telecommunication, Finolex Academy of Management and Technology, Ratnagiri
5. Worked as a **Judge** for **ELEKTRA 2k16 A Technical Paper Presentation competition** organized by EESA, Electronics department, Finolex Academy of Management and Technology, Ratnagiri
6. Worked as a **Judge** in **A Technical Paper Presentation competition 2k17** organized by, Electrical department, Finolex Academy of Management and Technology, Ratnagiri
7. Worked as a **Judge** for **TechSpring 2k17 A Technical Paper Presentation competition** organized by ASENT, Electronics & Telecommunication Department, Finolex Academy of Management and Technology, Ratnagiri
8. Worked as a **Judge** for **Brainwaves 2k17 A National Level Technical Paper Presentation competition** organized by Finolex Academy of Management and Technology, Ratnagiri

### **Projects / Software Development**

1. Developed a Stand alone Electronic Voting Machine with Fingerprint & Face Recognition as authentication tool.

## Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience
1.	Electronics circuit and devices	S.Y.E&TC (Sem - III)	1 Year
2.	Analog Electronis-I	S.Y.E&TC (Sem - III)	3 Years
3.	Analog Electronics-II	S.Y.E&TC (Sem - III)	3 Years
4.	Integrated circuits	T.Y.E&TC (Sem - V)	1 Year
5.	Electronic Devices and Circuits I	S.Y.E&TC (Sem - III)	1 Year
6.	Microprocessor LAB	T.Y.E&TC (Sem - V)	1 Year
7.	Optical Fiber Communications LAB	B.E. E&TC (Sem - VIII)	1 Year
8.	MINI-PROJECT LAB	T.Y. E&TC (Sem V & VI)	2 Years