

**Name** Miss Neha S. Sakhalkar,  
B.E.(Electronics),M.E.(Microelectronics)

Date of Birth 11/09/1987

Highest Qualification M.E.

Branch / Specialization Microelectronics

Designation Lecturer

Department Electronic and Telecommunication

Date of Joining the Institute 22/02/2010



### Work Experience

Working with Finolex academy of management and technology as lecturer from 22/02/2010 till date (5.5 years experience)

### Number of Papers Presented/Published in National/International Conferences Details

#### International Conferences : 2

1. Presented paper , "CORDIC based hardware for Sine-Cosine generation using VHDL", in 3rd International Conference on Emerging Trends and Research in Engineering and Technology on 6th & 7th April 2015, IBSS College of Engineering, Amravati, Maharashtra
2. Presented paper , "Design and Implementation of CORDIC Hardware for Sine-Cosine Generation using VHDL" on 27th & 28th March 2015, Shree Rayeshwar Institute of Engineering and Information Technology, Shiroda, Goa

### Number of Papers Presented/Published in National/International Journals

#### International Journals : 2

#### Details

1. Published paper , "CORDIC based hardware for Sine-Cosine Generation using VHDL" in International Journal of Pure and Applied Research in Engineering and Technology - ISSN: 2319-5078
2. Published paper , " Design and Implementation of CORDIC

Hardware for Sine-Cosine Generation using VHDL", in  
International Journal of Advances in Management, Technology  
and Engineering Sciences - Vol. 4, Issue 6, March 2015, ISSN:  
2249-7455

<b>Number of Workshops, STTPs Attended</b>	<b>2</b>
<b>Details</b>	<ol style="list-style-type: none"><li>1. Attended Two days ISTE workshop on 'Virtual Labs, Electronics ' Conducted by IIT Bombay and MHRD initiative on 9<sup>th</sup> to 10<sup>th</sup> April 2011.</li><li>2. Attended a Two-Week ISTE workshop on 'Basic Electronics' Conducted by IIT Bombay under the National Mission on Education through ICT (MHRD, Govt. of India) at Finolex Academy of Management &amp; Technology, Ratnagiri from 28th June to 8th July, 2011.</li></ol>
<b>Details of student projects Guided at Masters Level</b>	<b>NIL</b>
<b>Membership in Professional Bodies</b>	<b>NIL</b>
<b>Seminars/Lecture/ Invited Talks Delivered As Resource Person</b>	<b>NIL</b>
<b>Details</b>	<b>NIL</b>
<b>Awards / Honours / Prizes</b>	<b>NIL</b>
<b>Research / Sponsored / Consultancy Projects</b>	<b>NIL</b>