

**Name** MS.NEHA SHRIKANT SAKHALKAR  
B.E.(ETRX),M.E.(Microelectronics)

**Designation** Assistant Professor

**Department** EXTC

**Date of Joining the Institute** 22/02/2010

**Education** B.E.(Electronics)  
M.E.(Microelectronics)

**Branch / Specialization** Microelectronics

**Areas of Interest** Embedded Syestems, Integrated Circuits, VLSI, Image Processing

**Email** neha.sakhalkar@famt.ac.in



### Academic Appointments

Name of the University / Institute/ Industry	Duration	Designation
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**Finolex Academy of Management and Technology, Ratnagiri**

**From 22/02/10 to till date**

**Assistant Professor**

**Total Experience – 8 Years**

**Awards / Honors / Prizes**

1. **Cracked GATE 2013(ECE) with 91.17 percentile**

**Papers Published**

**Papers Presented/Published in National / International Journals – 02**

**International Journals – 02**

1. Published paper , "CORDIC based hardware for Sine-Cosine Generation using VHDL" in International Journal of Pure and Applied Research in Engineering and Technology - ISSN: 2319-5078

2. Published paper , " Design and Implementation of CORDIC Hardware for Sine-Cosine Generation using VHDL", in International Journal of Advances in Management,Technology and Engineering Sciences - Vol. 4, Issue 6, March 2015, ISSN: 2249-7455

## Papers Presented/Published in National/ International Conferences – 02

### International Conferences – 02

1. Presented paper , "CORDIC based hardware for Sine-Cosine generation using VHDL", in 3rd International Conference on Emerging Trends and Research in Engineering and Technology on 6th & 7th April 2015, IBSS College of Engineering, Amravati, Maharashtra
2. Presented paper , "Design and Implementation of CORDIC Hardware for Sine-Cosine Generation using VHDL" on 27th & 28th March 2015, Shree Rayeshwar Institute of Engineering and Information Technology, Shiroda, Goa

## Number of Workshops, Seminars, STTPs, FDPs Attended – 07

### STTPs and FDPs

1. Attended One Week Workshop on "Funding Schemes available for research proposals" organized by RDCC of Famt, Ratnagiri
2. Attended Two days ISTE workshop on 'Virtual Labs, Electronics ' Conducted by IIT Bombay and MHRD initiative on 9<sup>th</sup> to 10<sup>th</sup> April 2011.
3. Attended a Two-Week ISTE workshop on 'Basic Electronics' Conducted by IIT Bombay under the National Mission on Education through ICT (MHRD, Govt. of India) at Finolex Academy of Management & Technology, Ratnagiri from 28th June to 8th July, 2011.

### Conferences and Seminars

4. Attended 3rd International Conference on Emerging Trends and Research in Engineering and Technology on 6th & 7th April 2015, IBSS College of Engineering, Amravati, Maharashtra
5. Attended International Conference on Latest Trends in Engineering and Technology in Global R&D Sectors on 27th & 28th March 2015 at Shree Rayeshwar Institute of Engineering and Information Technology, Shiroda, Goa

### Two Week or One Week Workshops

6. Attended **One Week Workshop** on "**Funding Schemes Available for Research Proposals**" Organized by Research Development and Consultancy cell (RDCC) of Finolex Academy of Management and Technology, Ratnagiri from 13<sup>th</sup> February to 23<sup>rd</sup> April 2016
7. Attended a Two-Week ISTE workshop on 'Basic Electronics' Conducted by IIT Bombay under the National Mission on Education through ICT (MHRD, Govt. of India) at Finolex Academy of Management & Technology, Ratnagiri from 28th June to 8th July, 2011.

## Professional Membership

1. Indian Society of Technical Education (I.S.T.E)

## College Exam Duties

1. Worked as an **Assistant CAP coordinator** for the 2013 paper assessment at **FAMT MCA CAP**.
2. Worked as a **Senior Supervisor** of **2<sup>nd</sup> Half of 2012 Winter Exam**.
3. Carried out exam related duties such as **Moderator, Examiner and External Examiner** for conducting theory, practical and oral exam as appointed by Board of Examination, Mumbai University..
4. Worked as **Teacher in Charge** for Practical's/Viva since 2011

## Other Experience

### College Level Committees

1. Working as a **Departmental Member of Internal Complaint Committee**
2. Working as a **Departmental Member of Course Enrichment Committee**
3. Working as a **Departmental Member** of Women Development Cell
4. Working as a **Departmental Member** of ISTE Students chapter
5. Worked as departmental organizing **committee member** in 2nd National Conference on **"Recent Advances in Science and Technology"**
6. Working as a department coordinator for **NAAC Criteria 6 committee**

## Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience
1.	Integrated Circuits	T.E.EXTC SEM V	3 Years
2.	VLSI Design	T.E.EXTC SEM VI	3 Years
3.	Principle of Control Systems	S.E.EXTC SEM IV	2 Years
4.	Circuit Theory and Networks	S.E.EXTC SEM III	2 Years

5.	Digital Image Processing	B.E.EXTC SEM VIII	1 Year
6.	Continuous Time Signals and Systems	T.E.ETRX SEM V	2 Years
7.	Discrete Time Signals and Systems	T.E. ETRX SEM VI	2 Years
8.	Electrical Network Analysis an Systems	S.E.ETRX SEM III	2 Years
9.	Digital System Design II	S.E.ETRX SEM VI	1 Year
10.	Computer Organization	T.E.EXTC SEM VI	1 Year