

Name **MRS. RASHMI R. KULKARNI**  
**M.Tech in ELECTRONICS**  
**TECHNOLOGY**

Date of Birth	08/06/1978
Highest Qualification	M.Tech
Branch / Specialization	Electronics Technology
Designation	Asst. Professor
Department	Electronics and Telecommunication
Date of Joining the Institute	15/07/2011



### Work Experience

**Working with Finolex academy of management and technology as lecturer from 15/07/11 to till date (6 years experience)+ Previous Experience (4 Years)**

**Total 10 Years Experience**

### Number of Papers Presented/Published in National/ International Conferences

1. A paper titled "**Simple and low cost IC tester for Analog and Digital labs** " is Presented in the National conference on Recent Advances in Science and Technology(RAST-2016), held at FAMT, Ratnagiri, during February 17-18 , 2016, ISBN: 978-93-83046-31-7 Pages: 188-192
2. A paper titled "**High Speed Convolution and Deconvolution using Urdhav Triyagbhyam** " is Presented in the IEEE Computer Society Annual Symposium on VLSI held at IIT Madras , Chennai, during July 4-6 ,2011
3. A paper titled "**Convolution on FPGA using Vedic Mathematics**" is Presented in National Conference on Emerging Trends in Engineering, Technology and Architecture (NCETETA 2011) held at DYP Kolhapur.

### Number of Papers Presented/Published in National/ International Journals

1. A paper titled "**Simple and Low Cost IC Tester for Analog and Digital Labs**" is

published in Asian Journal of Convergence in Technology, VOL 2, Issue 3, In 2016,PP 1-6, e-ISSN:2350-1146

2. A paper titled "**32bit Floating Point Vedic Multiplier**" is published in International Organization Of Scientific Research(IOSR) Journal of VLSI and Signal Processing (IOSR-JVSP), VOL 6, Issue 2, April 2016,PP 16-20, e-ISSN: 2319-4200, p-ISSN: 2319-4197
3. A paper titled "**Comparison Among Different Adders** " is published in International Organization Of Scientific Research(IOSR) Journal of VLSI and Signal Processing(IOSR-JVSP), VOL 5, Issue 6,Ver:I, Nov.- Dec. 2015,PP 01-06,e-ISSN: 2319-4200,p-ISSN: 2319-4197
4. A paper titled "**Parallel Hardware Implementation of Convolution and Deconvolution**" is published in IOSR Journal of VLSI and Signal Processing, VOL 1, Issue 4, Nov.- Dec. 2012.
5. A paper titled "**Speedy Convolution using Vedic Mathematics**" is published in International Journal of Recent Trends of Engineering and Technology (IJTET) by ACEEE, USA
6. A paper titled "**Speedy Deconvolution using Vedic Mathematics**" is published in International of Science and Engineering Research (IJSER,France), Volume 2, Issue 5, May 2011.Published paper in IJSER is indexed by different ten well known e-publications.

#### Number of Workshops, STTPs Attended

1. Attended three days workshop on "**Embedded Systems and Robotics Design**" organized by I<sup>2</sup>IT Trust and society's PRALHAD P. CHHABRIA RESEARCH CENTER.
2. Attended Four week AICTE approved Workshop on "**Use of ICT in Education for Online and Blended Learning**", organized IIT Bombay.
3. Attended one week Workshop on "**Funding Schemes Available for Research Proposal**", organized by Finolex Academy of Management and Technology, Ratnagiri.
4. Attended one day "**Faculty Development Program**", organized by Information Technology department of Finolex Academy of Management and Technology, Ratnagiri.
5. Attended Two Week ISTE workshop on "**Pedagogy For Effective use of ICT in Engineering Education**" Conducted by IIT Bombay under the National Mission on Education through ICT.

6. Attended Two Week ISTE workshop on “**Signals and Systems**” Conducted by IIT Kharagpur under the National Mission on Education through ICT.
7. Attended 2-week ISTE Workshop on “**Introduction to Research Methodologies**” Under the National Mission on Education through ICT (MHRD, Govt. of India) Organized by IIT Bombay .
8. Attended One Week workshop on ‘**Embedded Using Linux ARM & Keil**’ Conducted by Antech Technology Foundation.
9. Attended 2 Day Workshop on “**Mobile Antenna – Fundamentals ,Design, Fabrication and Testing**” Organized by Dept. of Electronics and Telecommunication, Finolex Academy of Management and Technology, Ratnagiri.
10. Attended Two days Workshop on “**Network and Cyber security**”, organized by Finolex Academy of Management and Technology, Ratnagiri.
11. Attended Two days Workshop on **Linux**, organized by Finolex Academy of Management and Technology.
12. Attended one day workshop on “**Quality Education in Engineering and Technical Institution**” organized by ISTE local chapter FAMT, Ratnagiri.

#### Details of student projects Guided at Masters Level

NIL

#### Membership in Professional Bodies

Indian Society of Technical Education (**I.S.T.E**)

#### Seminars/Lecture/ Invited Talks Delivered As Resource Person

Conducted one day workshop on “**Vedic Mathematics and It's Applications in Engineering**” at Finolex Academy of Management and Technology, Ratnagiri.

#### Awards / Honours / Prizes

1. **SAP award of excellence**, in Four Week AICTE approved FDP by **IIT Bombay** on “**Use of ICT in Education for Online and Blended Learning**”. It is cash award of 5000Rs with certificate.
2. **Won first prize** in exam conducted in workshop of **Embedded Using Linux ARM**

**& Keil by Antech technology, pune.** Award is in form of Trophy and certificate.

### Research / Sponsored / Consultancy Projects

NIL

### College Exam Duties - Type of Work

1. Worked as a **Senior Supervisor** in **Engineering 2013 Summer Examination**.
2. Carried out exam related duties such as **Examiner and External Examiner** for conducting practical and oral examination.
3. Worked as Internal Assessment Examination conductor at department level.

### Other Experience

1. Worked as **Reviewer** for paper "Design of Squaring Circuit Using Vedic Mathematics" of **IEEE Transactions on Circuits and Systems II** (TCAS-II) Journal.
2. Designed **Website** for subjects Digital Electronics and Digital Circuit Design ( <https://digitalelectronicsforstudents.wordpress.com> ). This website touches to all necessary information of above mentioned subjects and helps students to confidently face examinations.
3. Designed **personal website** .(<https://rashmikulkarni78.wordpress.com>) to create web presence and to provide Online Educational Resource (OER) to student, Participated in creation of flipped classroom using personal website and YouTube.
4. Worked as Girl's Hostel warden.