

Name **MRS. RASHMI R. KULKARNI**
MTech.(Electronics Technology), E&TC

Designation Assistant Professor

Department Electronics and Telecommunication

Date of Joining the Institute 15/07/2011

Education M.Tech

Branch / Specialization Electronics Technology

Areas of Interest Digital Electronics

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Academic Appointments

Name of the University / Institute/ Industry	Duration	Designation
Finolex Academy of Management and Technology, Ratnagiri	From 15/07/11 to till date	Assistant Professor

Total Experience – 7 Years

Awards / Honors / Prizes

1. **SAP award of excellence**, in Four Week AICTE approved FDP by **IIT Bombay** on "**Use of ICT in Education for Online and Blended Learning**". It is cash award of 5000Rs with certificate.
2. **Won first prize** in exam conducted in workshop of **Embedded Using Linux ARM & Keil by Antech technology, pune**. Award is in form of Trophy and certificate.

Seminars/Lecture/Invited Talks Conducted

1. Conducted Two days workshop on "**PCB Design**" at Finolex Academy of Management and Technology, Ratnagiri.
2. Conducted one day workshop on "**Vedic Mathematics and It's Applications in Engineering**" at Finolex Academy of Management and Technology, Ratnagiri.

Papers Published

Papers Presented/Published in National / International Journals - 06

International Journals – 05

National Journals – 01

1. A paper titled "**Simple and Low Cost IC Tester for Analog and Digital Labs**" is published in Asian Journal of Convergence in Technology, VOL 2, Issue 3, In 2016,PP 1-6, e-ISSN: 2350-1146
2. A paper titled "**32bit Floating Point Vedic Multiplier**" is published in International Organization Of Scientific Research(IOSR) Journal of VLSI and Signal Processing (IOSR-JVSP), VOL 6, Issue 2, April 2016,PP 16-20, e-ISSN:2319-4200, p-ISSN:2319-4197
3. A paper titled "**Comparison Among Different Adders**" is published in International Organization Of Scientific Research(IOSR) Journal of VLSI and Signal Processing(IOSR-JVSP), VOL 5, Issue 6,Ver: I, Nov.- Dec. 2015,PP 01-06,e-ISSN:2319-4200,p-ISSN:2319-4197
4. A paper titled "**Parallel Hardware Implementation of Convolution and Deconvolution**" is published in IOSR Journal of VLSI and Signal Processing, VOL 1, Issue 4, Nov.- Dec. 2012.
5. A paper titled "**Speedy Convolution using Vedic Mathematics**" is published in International Journal of Recent Trends of Engineering and Technology (IJTET) by ACEEE, USA
6. A paper titled "**Speedy Deconvolution using Vedic Mathematics**" is published in International of Science and Engineering Research (IJSER,France), Volume 2, Issue 5, May 2011.Published paper in IJSER is indexed by different ten well known e-publications.

Papers Presented/Published in National/ International Conferences - 03

International Conferences – 01

1. A paper titled "**High Speed Convolution and Deconvolution using Urdhav Triyagbhyam**" is Presented in the IEEE Computer Society Annual Symposium on VLSI held at IIT Madras , Chennai, during July 4-6 ,2011

National Conferences – 02

1. A paper titled "**Simple and low cost IC tester for Analog and Digital labs**" is Presented in the National conference on Recent Advances in Science and Technology(RAST-2016), held at FAMT, Ratnagiri, during February 17-18 , 2016, ISBN: 978-93-83046-31-7 Pages: 188-192
2. A paper titled "**Convolution on FPGA using Vedic Mathematics**" is Presented in National Conference on Emerging Trends in Engineering, Technology and Architecture (NCETETA 2011) held at DYP Kolhapur.

STTPs and FDPs

1. Completed Online course on '**Basic Electrical Circuits**' offered by IIT Madras, an online learning initiative of **Indian Institute of Technology Bombay**. This online course was conducted between 24 July – 15 September 2017.
2. Completed **Faculty Development Program (FDP)** on "**Career Opportunities and Self Dependence**" organized by ITSA and Lemures Technologies on 23rd September 2016
3. Attended Four Week AICTE approved **FDP** by IIT Bombay on "**Use of ICT in Education for Online and Blended Learning**" from 2nd May to 10th July 2016
4. Attended Two Week ISTE workshop on "**Pedagogy For Effective use of ICT in Engineering Education**" Conducted by IIT Bombay under the National Mission on Education through ICT.

Conferences and Seminars

5. Attended 2nd **National Conference on Recent Advances in Science and Technology (RAST)** organized by Finolex Academy of Management and Technology, Ratnagiri from 17th to 18th February 2016
6. Attended three days, **IEEE Computer Society Annual Symposium** on VLSI held at IIT Madras , Chennai, during July 4-6 ,2011
7. Attended Two days National Conference on Emerging Trends in Engineering, Technology and Architecture (NCETETA 2011) held at DYP Kolhapur.

Two Week or One Week Workshops

8. Attended one week Workshop on "**Funding Schemes Available for Research Proposal**", organized by Finolex Academy of Management and Technology, Ratnagiri.
9. Attended Two Week ISTE workshop on "**Signals and Systems**" Conducted by IIT Kharagpur under the National Mission on Education through ICT.
10. Attended 2-week ISTE Workshop on "**Introduction to Research Methodologies**" Under the National Mission on Education through ICT (MHRD, Govt. of India) Organized by IIT Bombay .
11. Attended One Week workshop on '**Embedded Using Linux ARM & Keil**' Conducted by Antech Technology Foundation

Workshops

12. Attended 2 Day Workshop on "**RF and Antennas - From Theory to Practice**" Organized by Dept. of Electronics and Telecommunication, Finolex Academy of Management and Technology, Ratnagiri.

13. Attended three days workshop on "**Embedded Systems and Robotics Design**" organized by I²IT Trust and society's PRALHAD P. CHHABRIA RESEARCH CENTER.
14. Attended 2 Day Workshop on "**Mobile Antenna – Fundamentals ,Design, Fabrication and Testing**" Organized by Dept. of Electronics and Telecommunication, Finolex Academy of Management and Technology, Ratnagiri.
15. Attended Two days Workshop on "**Network and Cyber security**", organized by Finolex Academy of Management and Technology,Ratnagiri.
16. Attended Two days Workshop on **Linux**, organized by Finolex Academy of Management and Technology.
17. Attended one day workshop on "**Quality Education in Engineering and Technical Institution**" organized by ISTE local chapter FAMT, Ratnagiri.

Professional Membership

1. Indian Society of Technical Education (I.S.T.E)

College Exam Duties

1. Worked as a **Senior Supervisor** in Engineering 2013 Summer Examination.
2. Carried out exam related duties such as **Examiner and External Examiner** for conducting practical and oral examination.
3. Worked as **Internal Assessment Examination conductor** at department level.

Other Experience

College Level Committees

1. Working as a **Women Grievance Cell member** of Finolex Academy of Management and Technology, Ratnagiri.
2. Working as a **Mentorship coordinator** of EXTC department.
3. Worked as departmental organizing **committee member** in 2nd National Conference on "**Recent Advances in Science and Technology**"
4. Working as a department coordinator for **NAAC Criteria 2 committee**
5. Worked as Girl's Hostel warden for 4 years.

Judge Panel / Staff Selection Committee / Subject Expert

1. Worked as a **Reviewer of journal IEEE Transactions on Circuits and Systems II (TCAS-II) in 2011.**

Projects / Software Development

1. Designed **Website** for subjects Digital Electronics and Digital Circuit Design (<https://digitalelectronicsforstudents.wordpress.com>). This website touches to all necessary information of above mentioned subjects and helps students to confidently face examinations.
2. Designed **personal website** .(<https://rashmikulkarni78.wordpress.com>) to create web presence and to provide Online Educational Resource (OER) to student, Participated in creation of flipped classroom using personal website and YouTube.

Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience
1.	Digital Electronics	SE EXTC (Sem-III)	4 Years
2.	Digital Logic Design	SE EXTC (Sem-III)	2Years
3.	Digital Circuit Design	SE ETRX (Sem-III)	5 Years
4.	Microprocessor and Peripherals	SE ETRX (Sem-IV)	4 Years
5.	Digital System Design	SE EXTC (Sem-III)	1 Year
6.	Digital System Design-I	SE ETRX (Sem-III)	2 Years
7.	Digital System Design-II	SE ETRX (Sem-IV)	2 Years
8.	Digital Design-I	SE ETRX (Sem-III)	2 Years
9.	Digital Design-II	SE ETRX (Sem-IV)	2 Years