

Name **Ms.Mansi Shriprakash Kolwankar**

Designation Assistant Professor
Department Electronics & Telecommunication Engineering
Date of Joining the Institute 01/08/2008
Education B.E.(E & TC), RM CET, Devrukh, 2008
M. E(E & TC), Appeared
Branch / Specialization Electronics & Telecommunication
Areas of Interest Digital Communication, Data Compression and Encryption, Speech Recognition
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Academic Appointments

Name of the University / Institute / Industry	Duration	Designation
Finolex Academy of Management & Technology, Ratnagiri	From 01/08/2008 to till date	Assistant Professor

Total Experience – 09.5 Years

Seminars/Lecture/Invited Talks Conducted

1. Conducted a session on '**Do's and Don'ts in Project Work –Discussion through Case Studies**' as a resource person for B. E. – E & TC students on 22nd July, 2017 at Finolex Academy of Management and Technology Ratnagiri.
2. Conducted workshop on "**Matlab and Simulink**" on 3rd – 4th October 2015 organized by Department of Electronics and Telecommunication, Finolex Academy of Management and Technology, Ratnagiri.

Papers Published

Papers Presented/Published in National / International Journals - 03

International Journals – 01

1. M.S.Kolwankar,M.R.Dixit "**English to Marathi Audio Dictionary using speech Recognition**", International academic conference on electrical,electronics & robotics,Pune.volume 3.

National Journals – 02

1. B.Y.Sathe,M.S.Kolwankar'**Image fusion methods for medical & general purpose applications**' in Asian Journal
2. M.S.Kolwankar,V.A.Patil,H.S.Bodas '**Analysis of PAPR in IFDMA system using norm technique**' in Asian Journal

Papers Presented/Published in National/ International Conferences - 03

International Conferences – 01

1. M.S.Kolwankar, "**English to Marathi Audio Dictionary using speech Recognition**", International academic conference on electrical,electronics& robotics,Pune.2013-14

National Conferences – 02

1. B.Y.Sathe,M.S.Kolwankar'**Image fusion methods for medical & general purpose applications**' in Recent Advances in Science & Technology,18-19 feb 2016
2. M.S.Kolwankar,V.A.Patil,H.S.Bodas '**Analysis of PAPR in IFDMA system using norm technique**' in Recent Advances in Science & Technology,18-19 feb 2016

Number of Workshops, Seminars, STTPs, FDPs Attended – 11

STTPs,FDPs, Conferences,Seminars & Workshops

1. Attended a workshop and worked as a Coordinator for Two days' workshop on '**RF and Antennas: From Theory to Practice**' at Finolex Academy of Management & Technology, Ratnagiri from 11th August and 12th August, 2017.
2. Attended a workshop and worked as a Coordinator for 3-days workshop on '**Networking Fundamentals with Hands-on using NS-2/NS-3 and Unified Communication**' at Finolex Academy of Management & Technology, Ratnagiri from 27th January to 29th January, 2017.
3. Attended 2nd **National Conference** on '**Recent Advances in Science and Technology (RAST-2K16)**' organized by Finolex Academy of Management and Technology, Ratnagiri from 17th to 18th February 2016.
4. Attended **One Week Workshop** on "**Funding Schemes Available for Research Proposals**" organized by Research Development and Consultancy cell (RDCC) of Finolex Academy of Management and Technology, Ratnagiri from 13th February to 23rd April 2016

5. Attended **Two week ISTE workshop** on "**Pedagogy for Effective use of ICT in Engineering Education**" at Finolex Academy of Management & Technology, Ratnagiri from 05th January to 21th January, 2015
6. Attended **Two week ISTE workshop** on "**Signal and Systems**" Conducted by IIT Bombay at Finolex Academy of Management & Technology, Ratnagiri from 02nd January to 12th January, 2014
7. Attended **Two week ISTE workshop** on "**Basic Electronics**" Conducted by IIT Bombay under the National Mission on Education through ICT at Finolex Academy of Management & Technology, Ratnagiri from 28th June to 8th July, 2011
8. Attended a Two-day workshop on '**Virtual Labs, Electronics**' Conducted by IIT Bombay in association with MHRD, (Govt. of India) at Finolex Academy of Management & Technology, Ratnagiri on 9th and 10th April, 2011.
9. Attended a Two-day workshop on '**Simulation of Electronic Circuits and Power Electronics Systems using SEQUEL**' Conducted by Prof. M. B. Patil, IIT Bombay at Finolex Academy of Management & Technology, Ratnagiri on 31st October and 1st November, 2009.
10. **Two days workshop** on "**Embedded System**" at Terna Engineering Collage, Navi Mumbai From 29th July to 30th July 2009..
11. Attended One day workshop on '**Quality Education in Engineering and Technical Institution**' at Finolex Academy of Management & Technology, Ratnagiri on 24th January, 2009.

Professional Membership

1. Indian Society of Technical Education (I.S.T.E): Life Member

College Exam Duties

1. Worked as a **Senior Supervisor** for **Winter 2014 Exam**.
2. Carried out exam related duties such as **Moderator, Examiner and External Examiner** for conducting theory, practical and oral exam as appointed by Board of Examination, Mumbai University.
3. Worked as a **result-checking committee member**.
4. Worked as a **coordinator** for scheduling and arrangement for conduction of departmental university Practical/Viva Exam.
5. Worked a **scrutiny committee member**.
6. Worked as an **Invigilator** for University theory exams.
7. CAP co-ordinator May 2017 and May 2017

Other Experience

College Level Committees

1. Working as a **departmental coordinator** of **Training and Placement** (TNP) of Finolex Academy of Management and Technology, Ratnagiri.
2. Working as a **Core Committee Member** of Alumini Association.
3. Worked as departmental organizing **committee member** in 2nd National Conference on **“Recent Advances in Science and Technology” (RAST-2K16)**.
4. Working as a department coordinator for **NAAC Criteria-1 committee**.

Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience
1.	Data Compression & Encryption	B.E.-E & TC (Sem - VII)	3 Years
2.	Optical Fiber Communication	B.E.-E & TC (Sem - VIII)	2 Years
3.	Control System	S.E.-E & TC (Sem - IV)	3 Years
4.	Circuits & transmission lines	S.E.-E & TC (Sem - III)	2 Year
5.	Digital Communication	T.E.-E & TC (Sem - V)	2 Year
6.	Principle of communication Eng	S.E-E & TC (Sem - IV)	2 Years
7.	Microprocessor	T.E.-I.T (Sem - V)	1 Year
8.	Microprocessor & Microcontroller	S.E.-IT (Sem - IV)	1 Year
9.	Digital Communication	T.E.-ETRX (Sem - V)	1 Year
10.	Digital Logic Design	S.E.-E & TC (Sem - III)	2 Years
11.	Electronic Hardware Workshop	T.E.-E & TC (Sem - V)	1 Years
12.	Software Simulation	S.E-E & TC(Sem-IV)	2 years