

<b>Name</b>	<b>MR. Giri S. Kulkarni</b>
Date of Birth	09/01/1968
Highest Qualification	M.TECH (Micro Electronics & VLSI )
Branch / Specialization	Electronics & Telecommunication
Designation	Associate Professor
Department	Electronics & Telecommunication
Date of Joining the Institute	14/07/1997



**Work Experience** Working with Finolex academy of management and Technology as Associate Professor from 14/07/1997 to till date (Teaching Experience : 22 Years , Industry Experience : 2 Years )

**Number of Papers Presented/Published in National/ International Conferences** **1**

**Details**

- 1. Paper presented titled "Highly Robust Nanoscale Planar Double-Gate MOSFET device and SRAM Cell Immune to Gate-Misalignment and Process Variations" in 2<sup>nd</sup> IEEE International Workshop on Electron Devices and Semiconductor Technology (IEDST) 2009.**

**Number of Workshops, STTPs Attended** **12**

**Details**

- 1. Attended Two Day workshop on 'Mobile Antennas-Fundamentals, Design, Fabrication and Testing' conducted by Department of E and TC and Electronics of FAMT on 01-02 August,2015.**
- 2. Worked as Remote Centre coordinator for Two Week ISTE workshop on 'Fluid Mechanics' Conducted by IIT Kharagpur under the National Mission on Education through ICT from 20/05/2014 to 30/05/2014.**
- 3. Worked as Teaching Assistant for Two Week ISTE workshop on 'Signals and Systems' Conducted by IIT**

**Kharagpur under the National Mission on Education through ICT from 02/01/2014 to 12/01/2014**

- 4. Worked as Teaching Assistant for Two Week ISTE workshop on 'Analog Electronics' conducted by IIT Kharagpur under the National Mission on Education through ICT from 04/06/2013 to 14/06/2013.**
- 5. Worked as A-View and Network Coordinator for Two Week ISTE workshop on 'Engineering Mechanics' conducted by IIT Bombay under the National Mission on Education through ICT from 26/11/2013 to 06/12/2013.**
- 6. Attended Two Week ISTE workshop on 'Introduction to Research Methodologies' Conducted by IIT Bombay under the National Mission on Education through ICT from 25/06/2012 to 04/07/2012.**
- 7. Attended Two day workshop on 'Virtual Labs, Electronics' conducted by Electrical Department, IIT Bombay on April 09 and 10, 2011.**
- 8. Attended Two Day workshop on 'Simulation of Electronic Circuits & Power Electronic Systems using SEQUEL' Organized by Department of E and TC, FAMT on October 31<sup>st</sup> and 1<sup>st</sup> November, 2009.**
- 9. Attended Three day Seminar cum Workshop on 'VLSI Design and Technologies' conducted by Shaha & Anchor Kutchhi Engineering College from 12 to 14 August 2004.**
- 10. Attended one day workshop on 'Advances in Micro Electronics' conducted by IIT Bombay and FAMT on 15<sup>th</sup> March 2003.**
- 11. Attended Three day workshop on 'Design of VLSI systems and its Applications' conducted by ISS College of IT and Engineering for Women from February 16 to 18, 2001.**
- 12. Attended Two day Seminar on 'Reprogramming of Computer Education in Engineering/Polytechnics' organized by SGGs Nanded and C-DAC Pune at C-DAC Pune on August 10-11, 1996.**

**Membership in Professional Bodies**

- 1. Indian Society of Technical Education (I.S.T.E)**

**Membership number : LM 29422**