

Name	MR. Ashish B. Vartak M.Tech. (Electronics and Telecommunication)
Designation	Assistant Professor
Department	Electronics Engineering
Date of Joining the Institute	14/03/2012
Education	B.Tech. Electronics & Telecommunication Engineering, Dr. B.A.T.U. Lonere, 2005 M.Tech. Electronics & Telecommunication Engineering, Dr. B.A.T.U. Lonere, 2009
Branch / Specialization	Electronics & Telecommunication
Areas of Interest	Computer Networking, Digital Image Processing
Email	Ashish.vartak@famt.ac.in



Academic Appointments

Name of the University / Institute/ Industry	Duration	Designation
Dr. Babasaheb Ambedkar Technological University, Lonere	22/08/2005 to 09/06/2006	Lecturer
Dr. Babasaheb Ambedkar Technological University, Lonere	10/07/2006 to 20/05/2007	Lecturer
Dr. Babasaheb Ambedkar Technological University, Lonere	03/07/2008 to 31/05/2009	Lecturer
Gharda Institute of Technology	20/07/2009 to 13/03/2012	Assistant Professor
Finolex Academy of Management and Technology, Ratnagiri	14/03/2012 to till date	Assistant Professor

Total Experience – 11 Years

Awards / Honors / Prizes

1. Elite Certificate in NPTEL Online Certification for the subject **"Basic Electrical Circuits"** held in July to September 2017
2. SAP Award of Excellence one of the Top Performer For Four Week AICTE approved FDP by IIT Bombay on **"Use of ICT in Education for Online and Blended Learning"** from 2nd May to 10th July 2016

Papers Published

Papers Presented/Published in National / International Journals - 05

International Journals – 05

1. Paper titled **"Audio based Bird Species Recognition Using Naïve Bayes Algorithm"** published in International Journal of Modern Trends in Engineering and Research,(ISSN (Online) : 2393-8161),Volume 5 Issue 1,pp.117-124.
2. Paper titled **"Composite Structure Fractal Wideband Monopole Antenna for ISM Band Applications"** published in International Journal of Modern Trends in Engineering and Research,(ISSN (Online) : 2393-8161),Volume 4 Issue 7,pp.275-280.
3. Paper titled **"High Resolution DPW Modulator based on FPGA with Linear behavior of system"** published in ASIAN JOURNAL OF CONVERGENCE IN TECHNOLOGY (AJCT ISSN : 2350 - 1146).
4. Paper titled **"Stepped and Hairpin Combined Wideband Bandpass Filter for ISM Band"** published in ASIAN JOURNAL OF CONVERGENCE IN TECHNOLOGY (AJCT ISSN : 2350 - 1146).
5. Paper titled **"Brain Tumor Classification into Normal and Abnormal Using PCA and PNN Classifier"** published in International Journal of Engineering Development and research, (ISSN 2321-9939, Volume 4, Issue 1, January 2016), pp. 97-102.

Papers Presented/Published in National/ International Conferences - 06

International Conferences – 01

1. Paper Presented titled **"JOP: Java optimised Processor for embedded Real time System"** for the International Seminar on 'IT in academics' organized by Sinhgad Institute of management Pune, held from 20th to 22nd February 2009.

National Conferences – 05

2. Paper presented titled **"High Resolution DPW Modulator based on FPGA with Linear behavior of system"** at 2nd National Conference on Recent Advances in Science and Technology (RAST) organized by Finolex Academy of Management and Technology, Ratnagiri, on 17th and 18th February 2016. Paper Published in Proceeding of **Conference ISBN: 978-93-83046-31-7**, pp.147-151
3. Paper presented titled **"Stepped and Hairpin Combined Wideband Bandpass Filter for ISM Band"** at 2nd National Conference on Recent Advances in Science and Technology (RAST) organized by Finolex Academy of Management and Technology, Ratnagiri, on 17th and 18th February 2016. Paper Published in Proceeding of **Conference ISBN: 978-93-83046-31-7**, pp.155-158

4. Paper presented titled **"ECG rhythm analysis using real time QRS Detection algorithm"** for the the national conference on 'AISPC-2009' organized by Karmaveer Baburao Ganpatrao Thakare College of Engineering Nashik during 23rd – 24th January 2009.
5. Paper Presented titled **"Spectral Analysis of HRV signal"** for the National level Colloquium 'Technoevolution-2K9' organized by Amrutvahini College of Engineering Sangamner on 29th January 2009.
6. Paper presented titled **"Implementation of RISC architecture on FPGA platform"** for the National Conference on 'Electronics Communication & Automation-08' organized by College of Engineering, Pandharpur, held on 23rd and 24th December 2008.

Number of Workshops, Seminars, STTPs, FDPs Attended – 17

STTPs and FDPs

1. Attended One Week AICTE-ISTE approved Induction/Refresher program organized by Finolex Academy of Management and Technology, Ratnagiri on **"Digital Image Processing- Issues, Chalanges and Research Avauues"** from 4th June to 8th June 2018
2. Attended One day Seminar organized by Finolex Academy of Management and Technology, Ratnagiri on **"An introduction to Speech Technology: Automatic Speech Recognition and Text-to-Speech Synthesis"** on 31st January 2018.
3. Attended Four Week AICTE approved FDP by IIT Bombay on **"Use of ICT in Education for Online and Blended Learning"** from 2nd May to 10th July 2016
4. Attended Two week AICTE sponsored Faculty Development Program on **"Select Topics in Signal Processing and Machine Intelligence with Hands-on"** conducted during 23rd May to 3rd June, 2016 organized by Department of Electronics and Telecommunication Engineering, College of Engineering, Pune – 411005.
5. Attended Two Day ISTE STTP on **'Cisco Networking'** Conducted by TechKriti IIT Kanpur and Azure Syntet at Finolex Academy of Management & Technology, Ratnagiri on 3rd and 4th February, 2016
6. Attended One Day FDP On **'Self-Dependence and advantages of Technology and Digital Marketing'** Conducted by Lemures Technologies and IEEE Delhi on 23rd September 2016.

Conferences and Seminars

7. Attended 2nd **National Conference on Recent Advances in Science and Technology (RAST)** organized by Finolex Academy of Management and Technology, Ratnagiri from 17th to 18th February 2016

Two Week or One Week Workshops

8. Attended **One Week Workshop** on **"Funding Schemes Available for Research Proposals"** Organized by Research Development and Consultancy cell (RDCC) of Finolex Academy of Management and Technology, Ratnagiri from 13th February to 23rd April 2016
9. Attended **Two Week** ISTE workshop on **'Pedagoggy for Effective Use of ICT in Engineering Education'** Conducted by IIT Bombay under the National Mission on Education through ICT from 5th January 2015 to 31st January 2015..

10. Attended **Two Week ISTE workshop** on '**Introduction to Designs of Algorithm**' Conducted by IIT Kharagpur under the National Mission on Education through ICT from 27th April to 30th May 2015.
11. Attended **Two Week ISTE workshop** on '**Signals and Systems**' Conducted by IIT Bombay under the National Mission on Education through ICT from 2nd January to 12th January 2014.
12. Attended **Two Week ISTE workshop** on '**Computer Programming**' Conducted by IIT Bombay under the National Mission on Education through ICT from 16—21 June 2014.
13. Attended **Two Week ISTE workshop** on '**Computer Network**' Conducted by IIT Bombay under the National Mission on Education through ICT from 28th to 5th July 2014
14. Attended **Two Week ISTE workshop** on '**Analog Electronics**' Conducted by IIT Bombay under the National Mission on Education through ICT from 01-05 April 2013..

Workshops

15. Attended 2 Day **ISTE Workshop** for Teachers on "**Aakash for Education**" Conducted by IIT Bombay under the National Mission on Education through ICT from 10th to 11th November 2012
16. Attended 2 Day ISTE Workshop for Teachers on "**Simulation of Electronics circuits and power electronics system using SEQUEL**" Conducted by IIT Bombay under the National Mission on Education through ICT from 31st October and 1st November 2009.
17. Attended Three Day workshop on '**Advanced Digital Signal Processing**' Conducted by Amrutvahini College of Engineering Sangamner.

Professional Membership

1. Indian Society of Technical Education (I.S.T.E)

Other Experience

College Level Committees

1. Working as a **Departmental Mentorship co-ordinator** of **Mentorship Committee** of Finolex Academy of Management and Technology, Ratnagiri.
2. Worked as organizing **committee member** in 2nd National Conference on "**Recent Advances in Science and Technology**"
3. Working as a department coordinator for **NAAC Criteria 2 committee**
4. Working as a department coordinator for **NAAC Criteria 5 committee**
5. Working as **committee member** of **Students Affairs Committee**.

Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience
1.	Advanced Networking Technologies	B.E. Electronics (Sem - VIII)	6 Year
2.	Computer Communication Networks	B.E. Electronics (Sem - VII)	4 Years
3.	Digital Image Processing	B.E. Electronics (Sem - VII)	2 Years
4.	Circuit Theory	S.E. Electronics (Sem - III)	5 Years
5.	Electrical Networks analysis & Synthesis	S.E. Electronics (Sem - III)	2 Years
6.	Computer Organisation	T.E. Electronics (Sem - VI)	2 Years