

Name ASMITA ANIL CHAVAN (ME EXTC)

Designation Assistant Professor

Department EXTC

Date of Joining the Institute 30/07/2012

Education Master of Engineering in EXTC

Branch / Specialization EXTC

Areas of Interest Electronics and Telecommunication

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Academic Appointments

Name of the University / Institute/ Industry	Duration	Designation
Finolex Academy of Management and Technology, Ratnagiri	From 30/07/2012 to 31/05/2013 and 01/07/2014 to till date	Assistant Professor

Total Experience – 4.5 Years

Papers Published

Papers Presented/Published in National / International Journals - 02

National Journals – 02

1. Paper titled **“REVIEW OF AODV AND DSDV ROUTING PROTOCOLS IN MANET”** published in ASIAN JOURNAL OF CONVERGENCE IN TECHNOLOGY (AJCT ISSN : 2350 - 1146).
2. Paper titled **“IMAGE FUSION METHODS FOR MEDICAL AND GENERAL PURPOSE APPLICATIONS”** published in ASIAN JOURNAL OF CONVERGENCE IN TECHNOLOGY (AJCT ISSN : 2350 - 1146).

Papers Presented/Published in National/ International Conferences - 03

International Conferences – 01

1. Paper presented titled **“PERFORMAMACE ANALYSIS OF AODV AND DSDV ROUTING PROTOCOL IN MANET AND MODIFICATIONS IN AODV AGAINST BLACK HOLE ATTACK”** at 7th INTERNATIONAL CONFERENCE ON CLOUD COMPUTING AND VIRTUALIZATION 2016 organized by Thakur College of Engineering, Mumbai on 26th – 27th February, 2016.

National Conferences:02

2. Paper presented titled **“REVIEW OF AODV AND DSDV ROUTING PROTOCOLS IN MANET”** at 2nd National Conference on Recent Advances in Science and Technology (RAST) organized by Finolex Academy of Management and Technology, Ratnagiri on 17th – 18th February, 2016.
3. Paper presented titled **“IMAGE FUSION METHODS FOR MEDUCAL AND GENERAL PURPOSE APPLICATIONS”** at 2nd National Conference on Recent Advances in Science and Technology (RAST) organized by Finolex Academy of Management and Technology, Ratnagiri on 17th – 18th February, 2016.

Number of Workshops, Seminars, STTPs, FDPs Attended – 6

STTPs and FDPs

1. Participated in seminar on Faculty development program at Finolex Academy of Management and Technology, Ratnagiri on 23 September 2016
2. Attended Two Week ISTE STTP workshop on “Designs of Algorithm” Conducted by IIT Kharagpur under the National Mission on Education through ICT on 27th April to 30th May 2015.
3. Attended Two Week ISTE workshop on “Control Systems” Conducted by IIT kharagpur on 2nd to 12th 2014

One week workshop

4. Attended One Week Workshop on “Funding Schemes Available for Research Proposals” Organized by Research Development and Consultancy Cell (RDCC) of Finolex Academy of Management and Technology, Ratnagiri on 13th Feb. to 23rd April 2016

Workshops

5. Particiapted in 3 day workshop on “Embedded Systems and Robotics Design” at Finolex Academy of Management and Technology, Ratnagiri on 27th to 29th 2017.
6. Attended 2 Day ISTE Workshop for Teachers on “Aakash for Education” Conducted by IIT Bombay under the National Mission on Education through ICT on 10th – 11th 2012.

College Exam Duties

1. Worked as an **Assistant CAP coordinator** for Dec-2016 paper assessment at **FAMT Engineering CAP.**

Other Experience

College Level Committees

1. Internal Assessment conduction(EXTC dept.)
2. Departmental Timetable committee (EXTC dept.)
3. Worked as a **Member (departmental head-Electronics Engineering)** of Course enrichment committee of FAMT.
4. Worked as **Teacher in charge** of **ESTA (Electronics Department)**.
5. Worked as departmental organizing **committee member** in 2nd National Conference on **“Recent Advances in Science and Technology”**.
6. Worked as **Teacher in Charge** for Practical's/Viva.
7. Worked as a **member** of departmental time table committee (Electronics Engineering).

Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience
1.	Logic Design	SE/IT/SEM-III	1 Year
2.	Television Engineering	TE/EXTC/SEM-VI	1 Year
3.	Information Theory and Coding	SE/IT/SEM-IV	1 Year
4.	Electronic Instruments and Measurements	SE/ETRX/SEM-III	3 Years
5.	Mobile Communication	SE/ETRX/SEM-VIII	1 Year
6.	Fundamentals of Communication Engineering	SE/ETRX/SEM-IV	1 Year
7.	Basics of Analog and Digital Communication system	SE/ETRX/SEM-IV	1 Year
8.	Advanced Instrumentation System	TE/ETRX/SEM-VI	1 Year
9.	Basics of Electronic Circuits	SE/ETRX/SEM-III	1 Year