

Name ASMITA ANIL CHAVAN (ME EXTC)

Designation Assistant Professor
Department EXTC
Date of Joining the Institute 30/07/2012
Education Master of Engineering in EXTC
Branch / Specialization EXTC
Areas of Interest Electronics and Telecommunication
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Academic Appointments

Name of the University / Institute/ Industry	Duration	Designation
Finolex Academy of Management and Technology, Ratnagiri	From 30/07/2012 to 31/05/2013 and 01/07/2014 to till date	Assistant Professor
Total Experience – 5 Years		

Papers Published

Papers Presented/Published in National / International Journals - 02

National Journals – 02

1. Paper titled “**REVIEW OF AODV AND DSDV ROUTING PROTOCOLS IN MANET**” published in ASIAN JOURNAL OF CONVERGENCE IN TECHNOLOGY (AJCT ISSN : 2350 - 1146).
2. Paper titled “**IMAGE FUSION METHODS FOR MEDICAL AND GENERAL PURPOSE APPLICATIONS**” published in ASIAN JOURNAL OF CONVERGENCE IN TECHNOLOGY (AJCT ISSN : 2350 - 1146).

Papers Presented/Published in National/ International Conferences - 03

International Conferences – 01

1. Paper presented titled “**PERFORMAMACE ANALYSIS OF AODV AND DSDV ROUTING PROTOCOL IN MANET AND MODIFICATIONS IN AODV AGAINST BLACK HOLE**”

ATTACK” at 7th INTERNATIONAL CONFERENCE ON CLOUD COMPUTING AND VIRTUALIZATION 2016 organized by Thakur College of Engineering, Mumbai on 26th – 27th February, 2016.

National Conferences:02

2. Paper presented titled **“REVIEW OF AODV AND DSDV ROUTING PROTOCOLS IN MANET”** at 2nd National Conference on Recent Advances in Science and Technology (RAST) organized by Finolex Academy of Management and Technology, Ratnagiri on 17th – 18th February, 2016.
3. Paper presented titled **“IMAGE FUSION METHODS FOR MEDUCAL AND GENERAL PURPOSE APPLICATIONS”** at 2nd National Conference on Recent Advances in Science and Technology (RAST) organized by Finolex Academy of Management and Technology, Ratnagiri on 17th – 18th February, 2016.

Number of Workshops, Seminars, STTPs, FDPs Attended – 10

STTPs and FDPs

1. Attended AICTE-ISTE approved one week STTP on **“Digital Image Processing Issues, Challenges and Research Avenues”** Organized by IT department, Finolex Academy of Management and Technology, Ratnagiri.
2. Participated in ISTE approved One Week STTP on **‘Designing IOT Based Applications for Smart Cities’**, conducted by Department of Information Technology, FAMT, Ratnagiri. (Date: 29/01/2018 - 02/02/2018)
3. Participated in AICTE approved Faculty Development Programme on **‘Pedagogy for Online and Blended Teaching-Learning Process’** conducted by IIT Bombay (Date: 14/09/2017-12/10/2017)
4. Participated in AICTE approved Faculty Development Programme on **‘Foundation Program in ICT for Education’** conducted by IIT Bombay (Date: 03/08/2017 - 07/09/2017)
5. Attended Seminar on Faculty development program at Finolex Academy of Management and Technology, Ratnagiri on 23 September 2016
6. Participated in Two Week ISTE STTP workshop on **“Designs of Algorithm”** Conducted by IIT Kharagpur under the National Mission on Education through ICT on 27th April to 30th May 2015.
7. Participated in Two Week ISTE workshop on **“Control Systems”** Conducted by IIT kharagpur on 2nd to 12th 2014

One week workshop

8. Attended One Week Workshop on **“Funding Schemes Available for Research Proposals”** Organized by Research Development and Consultancy Cell (RDCC) of Finolex Academy of Management and Technology, Ratnagiri on 13th Feb. to 23rd April 2016

Workshops

9. Participated in 3 day workshop on **“Embedded Systems and Robotics Design”** at Finolex Academy of Management and Technology, Ratnagiri on 27th to 29th 2017.

10. Attended 2 Day ISTE Workshop for Teachers on “**Aakash for Education**” Conducted by IIT Bombay under the National Mission on Education through ICT on 10th – 11th 2012.

Workshop/Seminar/Lecture/Invited Talks Delivered As Resource Person

1. Worked as Resource person of two days workshop on “**PCB Design**” at Dept. of Electronics and Telecommunication, Finolex Academy of Management and Technology, Ratnagiri
2. Worked as Co-ordinator for ‘**Training Program on Network Associates**’, at Dept. of Electronics and Telecommunication, Finolex Academy of Management and Technology, Ratnagiri

Award/ Honour/ Prizes

1. **Elite Grade** in NPTEL Course on 'Basic Electronics' (12 Week)

College Exam Duties

1. Worked as an **Assistant CAP coordinator** for Dec-2016 paper assessment at **FAMT Engineering CAP**.
2. Worked as **CAP coordinator** for May-2018 paper assessment at **FAMT Engineering CAP**.
3. Departmental **Internal Assessment Faculty Incharge** (A.Y 2017-18, 2018-19) for Electronics and Telecommunication Engineering Department .
4. Worked as **Faculty In-Charge** for Practical's/Viva.
5. **Result Checking Committee** of Sem III to VI (CBSGS and CBCGS), First half of 2017 Exam, Second half of 2017 Exam.
6. **Quality control over assessment work** of Sem III, IV, V, VI (CBGS and Old) of Quality control over assessment work of Sem III, IV, V, VI (CBGS and Old) of Second Half of 2015 Exam. , Second Half of 2017 Exam.,

Other Experience

College Level Committees

1. Working as **Faculty In-Charge** of Student Association **ASENT (Electronics and Telecommunication Engineering Department)**.
2. Member of **Sports Committee**
3. Member of **Discipline Committee for UTOPIA 2018**.
4. Departmental **Time Table Faculty In charge** (A.Y.2017-18) for Electronics and Telecommunication Engineering Department.
5. Member of **Document Verification Committee**.

6. Departmental Member of **Criteria V and VI**
7. Supporting member of **Online Assessment Training Program**
8. Worked as a Member (departmental head-Electronics Engineering) of **Course enrichment committee** of FAMT.
9. Worked as **Faculty In-Charge** of Student Association **ESTA (Electronics Department)**.
10. Worked as departmental **organizing committee member** in 2nd National Conference on **“Recent Advances in Science and Technology”**.
11. Worked as a member of departmental **time table committee** (Electronics Engineering Department).

Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience
1.	Microwave and RADAR Engineering	BE/EXTC/SEM-VII	1 Year
2.	Logic Design	SE/IT/SEM-III	2 Year
3.	Television Engineering	TE/EXTC/SEM-VI	2 Year
4.	Information Theory and Coding	SE/IT/SEM-IV	1 Year
5.	Electronic Instruments and Measurements	SE/ETRX/SEM-III	3 Years
6.	Mobile Communication	SE/ETRX/SEM-VIII	1 Year
7.	Fundamentals of Communication Engineering	SE/ETRX/SEM-IV	1 Year
8.	Basics of Analog and Digital Communication system	SE/ETRX/SEM-IV	1 Year
9.	Advanced Instrumentation System	TE/ETRX/SEM-VI	1 Year
10.	Basics of Electronic Circuits	SE/ETRX/SEM-III	1 Year