

<b>Name</b>	<b>MR. SURAJ ANIL SHETE B.E.(ELECTRONICS ENGINEERING),M.E.(ELECTRONICS ENGINEERING)</b>
Designation	Assistant Professor
Department	ELECTRONICS ENGINEERING
Date of Joining the Institute	05/08/2013
Education	B.E. (ELECTRONICS) 2012  M.E. (ELECTRONICS) 2015
Branch / Specialization	ELECTRONICS ENGINEERING
Areas of Interest	Robotics and Automation, Signals and Systems, Digital Image Processing, DSP, Computer Organization and Architecture.
Email	<a href="mailto:suraj.shete@famt.ac.in">suraj.shete@famt.ac.in</a>



### Academic Appointments

Name of the University / Institute/ Industry	Duration	Designation
<b>Finolex Academy of Management and Technology, Ratnagiri</b>	<b>From 05/08/2013 to till date</b>	<b>Assistant Professor</b>
<b>Total Experience – 4.5 Years</b>		

### Awards / Honors / Prizes

1. Selected as a member of Editorial Board for the International Journal of Research and Scientific Innovation (IJRSI)
2. Book published on "Video encoding with authentication using Multiwavelet Transform" in LAMBERT, Germany.
3. Book published on "Walsh Hadamard Transform: An effective PAPR reduction approach in OFDM" in LAMBERT, Germany.
4. Secured 1<sup>st</sup> position in "Avishkar 2016-17 college level research project convention" as a part of Research Convention of University of Mumbai under

**Engineering and Technology category held at FAMT Ratnagiri on 4<sup>th</sup> October 2016.**

- 5. Got Honour code certificate from IIT Bombay for successfully completion of course on "Technical Communication for Scientist and Engineers" on 4<sup>th</sup> May 2016.**
- 6. Secured A Grade** in Two Week ISTE STTP on 'Introduction to Designs of Algorithm' Conducted by IIT Kharagpur under the National Mission on Education through ICT from 27<sup>th</sup> April to 30<sup>th</sup> May 2015.

### **Seminars/Lecture/Invited Talks Conducted**

- 1. Conducted one day workshop on "Advancement in Digital Image Processing" in SGMCE, Mahagaon, Gadhinglaj, Kolhapur on 23<sup>rd</sup> January 2018.**
- 2. Conducted workshop on "Programming with Arduino" in Government Polytechnic Malvan on 3-4 March 2017.**

### **Papers Published**

#### **Papers Presented/Published in National / International Journals - 10**

#### **International Journals – 10**

- 1. Paper titled "Traffic Aware Coloring based Scheduling Scheme for Mobile Wireless Body Area Networks" Journal of Electronic Design Technology. 2017; 8(3): 8–11p.**
- 2. Paper titled "BIT ERROR RATE PERFORMANCE OF OFDM-QAM MODULATION AND WHT OFDM-QAM MODULATION USING RAYLEIGH AND RICIAN CHANNEL" in International Journal of Modern Trends in Engineering and Research (IJMTER) volume. 5, Issue (1), January 2018. DOI:[10.21884/IJMTER.2018.5007.ZTLQ9](https://doi.org/10.21884/IJMTER.2018.5007.ZTLQ9)**
- 3. Paper titled "AUDIO BASED BIRD SPECIES RECOGNITION USING NAÏVE BAYES ALGORITHM" in International Journal of Modern Trends in Engineering and Research (IJMTER) volume. 5, Issue (1), January 2018. DOI:[10.21884/IJMTER.2018.5020.SHOJV](https://doi.org/10.21884/IJMTER.2018.5020.SHOJV)**
- 4. Paper titled "Composite Structure Fractal Wideband Monopole Antenna for ISM Band Applications" in International Journal of Modern Trends in Engineering and Research (IJMTER) volume. 4, Issue (7), July 2017. DOI:[10.21884/IJMTER.2017.4241.SDIDG](https://doi.org/10.21884/IJMTER.2017.4241.SDIDG)**
- 5. Paper titled "WHT and Double WHT: An effective PAPR reduction approach in OFDM" in IEEE Xplore Digital Library DOI: [10.1109/ICAECCT.2016.7942577](https://doi.org/10.1109/ICAECCT.2016.7942577).**
- 6. Paper titled "Walsh hadamard Transform : A new approach for PAPR reduction in OFDM" in Asian Journal of Convergence in Technology (AJCT) volume II, Issue III, ISSN no.: 2350-1146.**

7. Paper titled **"Edge – Coupled DGS Composite Bandpass Filter for Wi-Max Application"** in International Journal of Modern Trends in Engineering and Research (IJMTER) volume. 2, Issue (11), November2015, pp. 109-113.
8. Paper titled **"A New Approach for Spoken Word Detection"** in International Journal of Modern Trends in Engineering and Research (IJMTER) volume. 2, Issue (09), September2015, pp. 196-201.
9. Paper titled **"PAPR reduction of OFDM signal using Walsh Hadmanrd transform"** in International journal of research in science and technology (IJRST) Volume 5 Issue no.1,January-march ISSN: 2249:0604.
10. Paper titled **"PAPR reduction techniques in OFDM: A brief review"** in International journal of Engineering Research and Technology (IJERT) ISSN: 2278-0181, VOLUME 3 Issue 4, April 2014.

### Papers Presented/Published in National/ International Conferences - 03

#### International Conferences – 02

1. Paper Presented titled **"WHT and Double WHT: An effective PAPR reduction approach in OFDM"** at 2016 IEEE International Conference on Advances in Electronics, Communication and Computer Technology (ICAECCT) Rajarshi Shahu College of Engineering, Pune India. Dec 2-3, 2016.
2. Paper presented titled **"Walsh Hadamard Transform: An effective PAPRR reduction approach in OFDM"** in International conference on Electronics & Computing 9-10 April,2015 at D.K.T.E.'s Textile and Engineering Institute, Ichalkaranji.

#### National Conferences – 01

1. Paper presented titled **"Walsh Hadamard Transform: A new approach for PAPR reduction in OFDM"** in National conference on Recent Advances in Science and Technology 18-19 Feb. 2016 at FAMT Ratnagiri.

### Number of Workshops, Seminars, STTPs, FDPs Attended – 22

1. Attended two day Saptahaant Shikshak Prashikshan on **Introduction to Machine Learning** conducted by IIT Kharagpur on 16<sup>th</sup> December,2017 and 17<sup>th</sup> December,2017.
2. Attended one day seminar on **An Introduction to Speech Technology : Automatic Speech Recognition and Text-to-Speech Synthesis** organized by Department of Electronics and Telecommunication Engineering on 31<sup>st</sup> January,2018.

3. Attended two days workshop on **DIGITAL IMAGE PROCESSING USING MATLAB** conducted by Department of EXTC Engineering at Sant Gajanan Maharaj College of Engineering, Mahagaon.
4. Successfully completed an AICTE approved FDP101x on **Foundation Program in ICT for education** conducted by IITB from August 3, 2017 to September 7, 2017.
5. Attended 2 days workshop on "**RF and Antennas- From Theory to Practice**" organized by Department of Electronics and Telecommunication Engineering FAMT Ratnagiri during 11<sup>th</sup> and 12<sup>th</sup> August, 2017.
6. Attended 1 day **MATLAB EXPO-2017** conducted by Mathworks.
7. Attended **2016 IEEE International Conference on Advances in Electronics, Communication and Computer Technology (ICAECCT)** at Rajarshi Shahu College of Engineering, Pune India. Dec 3, 2016.
8. Attended 2<sup>nd</sup> **National Conference on Recent Advances in Science and Technology (RAST)** organized by Finolex Academy of Management and Technology, Ratnagiri from 17<sup>th</sup> to 18<sup>th</sup> February 2016
9. Attended **International conference on Electronics & Computing** 9-10 April, 2015 at D.K.T.E.'s Textile and Engineering Institute, Ichalkaranji.
10. Attended 2 days workshop on '**Raspberry Pi**' conducted by Robocart.
11. Attended 3 days workshop on '**Embedded Systems and Robotic Design**' conducted by PPCRC, Pune.
12. Attended STTP approved Two day workshop on '**Internet Of Things**' conducted by Department of Computer Science and Engineering at Sant Gajanan Maharaj College of Engineering, Mahagaon.
13. Attended Two Week ISTE workshop on '**Designs of Algorithm**' Conducted by IIT Kharagpur under the National Mission on Education through ICT.
14. Attended Two Week ISTE workshop on '**Computer Network**' Conducted by IIT Bombay under the National Mission on Education through ICT.
15. Attended Two Week ISTE workshop on '**Control Systems**' Conducted by IIT Kharagpur under the National Mission on Education through ICT.
16. Attended Two Week ISTE workshop on '**Pedagogy for effective use of ICT in Engineering Education**' Conducted by IIT Bombay under the National Mission on Education through ICT.
17. Attended Two Week ISTE workshop on '**Signals and Systems**' Conducted by IIT Kharagpur under the National Mission on Education through ICT.

18. Attended one day orientation program on Inter collegiate Research Convention Avishkar 2014-15 conducted by FAMT Ratnagiri on behalf of Mumbai University.
19. Attended one-week Yoga course organized by FAMT Ratnagiri.
20. Attended one week workshop on "**Funding Schemes Available for Research Proposal**" conducted by FAMT Ratnagiri.
21. Attended Two week STTP on "**MATLAB & SIMULINK: Learning by Doing**" under ISTE-TEQIP-II conducted by Government College Of Engineering Karad from 06-06-2016 to 18-06-2016.
22. Successfully completed a course of study on "**Technical Communication for Scientist and Engineers**" offered by IIT BombayX between 11 January 2016-02 May 2016.

### Professional Membership

- Member of International Association of engineers [Member ID- 206271]
- Member of International Association of Online Engineers [Member ID-10138]

### College Exam Duties

1. Worked as a **Senior Supervisor of Engineering 2017 Winter Exam- Morning session (From 12/12/2017 to 16/12/2017)**.
2. Working as Departmental Exam in-charge of Sessional Exams for academic year 2017-2018.
3. Worked as a **Senior Supervisor of Engineering 2016 Winter Exam-Evening session (From 20/12/2016 to 29/12/2016)**.
4. Carried out exam related duties such as **Moderator, Scrutiny officer, Examiner and External Examiner** for conducting theory, practical and oral exam as appointed by Board of Examination, Mumbai University.

### Other Experience

#### College Level Committees

1. Working as Departmental coordinator for **NAAC criteria I**.
2. Working as Departmental coordinator for **NAAC criteria II**.

3. Working as a **Departmental Academic Feedback committee co coordinator** of Finolex Academy of Management and Technology, Ratnagiri.
4. Worked as departmental organizing **committee member** in 2nd National Conference on **“Recent Advances in Science and Technology”**

**Judge Panel / Staff Selection Committee / Subject Expert**

1. Worked as a Judge for Brainwaves 2k17 A National Level Technical Paper Presentation competition organized by Finolex Academy of Management and Technology, Ratnagiri
2. Worked as a Judge for Brainwaves 2k18 A National Level Technical Paper Presentation competition organized by Finolex Academy of Management and Technology, Ratnagiri

**Subject Taught**

<b>Sr. No.</b>	<b>Name of the Subject</b>	<b>Year/Branch/Semester</b>	<b>No. of times taught</b>
1.	Continuous Time Signals and Systems	T.E. Electronics (Sem -V)	1
2.	Robotics and Automation	B.E. Electronics (Sem-VIII)	4
3.	Signals and Systems	T.E. Electronics (Sem -V)	3
4.	Digital Signal Processing and Processors	T.E. Electronics (Sem -VI)	2
5.	Digital Image Processing	B.E. Electronics (Sem-VII)	3
6.	Computer Organization	T.E. Electronics (Sem -VI)	1
7.	Computer Organization and Architecture	S.E. IT (Sem-IV)	1
8.	Microprocessor Programming Lab	S.E. IT (Sem-IV)	1
9.	Mini Project Lab	T.E. Electronics (Sem -V & VI)	4
10.	Digital Circuit Design Lab	S.E. Electronics (Sem -III)	1