

Name MR. Ashish B. Vartak
M.Tech. (Electronics and
Telecommunication)

Date of Birth 28/104/1984

Highest Qualification M.Tech. (Electronics and
Telecommunication)

Branch /
Specialization **Digital Image Processing**

Designation Assistant Professor

Department Electronics Engineering

Date of Joining the
Institute 14/03/2012



Academic Appointments

Name of the University / Institute	Duration	Designation
Dr. Babasaheb Ambedkar Technological University, Lonere	22/08/2005 to 09/06/2006	Lecturer
Dr. Babasaheb Ambedkar Technological University, Lonere	10/07/2006 to 20/05/2007	Lecturer
Dr. Babasaheb Ambedkar Technological University, Lonere	03/07/2008 to 31/05/2009	Lecturer
Gharda Institute of Technology	20/07/2009 to 13/03/2012	Assistant Professor
Finolex Academy of Management and Technology, Ratnagiri	14/03/2012 to till date	Assistant Professor

Total Experience – 10 Years

Papers Presented/Published in National/ International Conferences

1. Paper presented titled **“High Resolution DPW Modulator based on FPGA with Linear behavior of system”** at 2nd National Conference on Recent Advances in Science and Technology (RAST) organized by Finolex Academy of Management and Technology, Ratnagiri.

Paper Published in Proceeding of **Conference ISBN: 978-93-83046-31-7**, pp.147-151

2. Paper presented titled **“Stepped and Hairpin Combined Wideband Bandpass Filter for**

ISM Band” at 2nd National Conference on Recent Advances in Science and Technology (RAST) organized by Finolex Academy of Management and Technology, Ratnagiri.

Paper Published in Proceeding of **Conference ISBN: 978-93-83046-31-7**, pp.155-158

3. Paper Presented titled **“JOP: Java optimised Processor for embedded Real time System”** for the International Seminar on ‘IT in academics’ organized by Sinhgad Institute of management Pune.
4. Paper presented titled **“ECG rhythm analysis using real time QRS Detection algorithm”** for the the national conference on ‘AISPC-2009’ organized by Karmaveer Baburao Ganpatrao Thakare College of Engineering Nashik.
5. Paper Presented titled **“Spectral Analysis of HRV signal”** for the National level Colloquium ‘Technoevolution-2K9’ organized by Amrutvahini College of Engineering Sangamner.
6. Paper presented titled **“Implementation of RISC architecture on FPGA platform”** for the National Conference on ‘Electronics Communication & Automation-08’ organized by College of Engineering, Pandharpur.

Papers Presented/Published in National/ International Journals

1. Paper titled **“High Resolution DPW Modulator based on FPGA with Linear behavior of system”** published in ASIAN JOURNAL OF CONVERGENCE IN TECHNOLOGY (AJCT ISSN : 2350 - 1146).
2. Paper titled **“Stepped and Hairpin Combined Wideband Bandpass Filter for ISM Band”** published in ASIAN JOURNAL OF CONVERGENCE IN TECHNOLOGY (AJCT ISSN : 2350 - 1146).
3. Paper titled **“Brain Tumor Classification into Normal and Abnormal Using PCA and PNN Classifier”** published in International Journal of Engineering Development and research, (ISSN 2321-9939, Volume 4, Issue 1, January 2016), pp. 97-102.

Number of Workshops, STTPs Attended

1. Attended Four Week AICTE approved FDP by IIT Bombay on **“Use of ICT in Education for Online and Blended Learning”** from 2nd May to 10th July 2016
2. Attended Two week AICTE sponsored Faculty Development Program on **“Select Topics in Signal Processing and Machine Intelligence with Hands-on”** conducted during 23rd May to 3rd June, 2016 organized by Department of Electronics and Telecommunication Engineering, College of Engineering, Pune – 411005.
3. Attended One Week Workshop on **“Funding Schemes Available for Research Proposals”** Organized by Research Development and Consultancy cell (RDCC) of Finolex Academy of Management and Technology, Ratnagiri.
4. Attended Two Day **ISTE STTP on ‘Cisco Networking’** Conducted by TechKriti IIT Kanpur

and Azure Synet at Finolex Academy of Management & Technology, Ratnagiri on 3rd and 4th February, 2016

5. Attended Two Week ISTE workshop on '**Designs of Algorithm**' Conducted by IIT Kharagpur under the National Mission on Education through ICT.
6. Attended Two Week ISTE workshop on '**Computer Network**' Conducted by IIT Bombay under the National Mission on Education through ICT.
7. Attended 2 Day ISTE Workshop for Teachers on "**Aakash for Education**" Conducted by IIT Bombay under the National Mission on Education through ICT.
8. Attended 2 Day ISTE Workshop for Teachers on "**Simulation of Electronics circuits and power electronics system using SEQUEL**" Conducted by IIT Bombay under the National Mission on Education through ICT
9. Attended Two Week ISTE workshop on '**Analog Electronics**' Conducted by IIT Bombay under the National Mission on Education through ICT.
10. Attended Two Week ISTE workshop on '**Signals and Systems**' Conducted by IIT Bombay under the National Mission on Education through ICT.
11. Attended Two Week ISTE workshop on '**Computer Programming**' Conducted by IIT Bombay under the National Mission on Education through ICT.
12. Attended Two Week ISTE workshop on '**Pedagogy for Effective Use of ICT in Engineering Education**' Conducted by IIT Bombay under the National Mission on Education through ICT.
13. Attended Three Day workshop on '**Advanced Digital Signal Processing**' Conducted by Amrutvahini College of Engineering Sangamner.

Students projects Guided at Masters Level

NIL

Membership in Professional Bodies

1. Indian Society of Technical Education (I.S.T.E)

Seminars/Lecture/ Invited Talks Delivered As Resource Person

NIL

Awards / Honours / Prizes

NIL

Research / Sponsored / Consultancy Projects

NIL

College Exam Duties - Type of Work

1. Worked as a **Senior Supervisor** of **BE 2014 Winter Exam**.
2. Carried out exam related duties such as **Moderator, Examiner and External Examiner** for conducting practical and oral exam as pointed by Board of Examination, Mumbai University.

Other Experience

1. Worked as organizing **committee member** in 2nd National Conference on "**Recent Advances in Science and Technology**"