

Name MR. MAHESH A. JADHAV
B.E.(ELECTRONICS), M.TECH

Designation Assistant Professor

Department Electronics Engineering

Date of Joining the Institute 08/05/2009

Education B.E Electronics 2006
M.Tech VLSI & Embedded System 2015
CDAC 2009

Branch / Specialization Electronics Engineering

Areas of Interest CMOS VLSI Design, Embedded System Design,
Electronics Devices and Circuits

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Academic Appointments

Name of the University / Institute / Industry	Duration	Designation
Finolex Academy of Management and Technology, Ratnagiri	From 08/05/2009 to till date	Assistant Professor

Total Experience – 8 Years

Awards / Honors / Prizes

1. **Top performer** of the programme for Four Week AICTE approved FDP by IIT Bombay on **"Use of ICT in Education for Online and Blended Learning"**. Won the **"Award of Excellence"** by SAP India Pvt.Ltd.

Seminars/Lecture/Invited Talks Conducted

1. Worked as a Coordinator for **Four days** workshop on **"Embedded C Programming & Application Development"** at Finolex Academy of Management & Technology, Ratnagiri from 4th August and 7th August, 2017.

Students projects Guided at Undergraduate Level

1. Smart Trolley System for Automated Billing Using RFID and ZIGBEE.
2. Quadcopter using Raspberry –Pi.
3. Wireless Animatronics Hand using Control Glove.

Papers Published

Papers Presented/Published in National / International Journals - 04

International Journals – 04

1. Paper Presented titled **“Performance Evaluation of Single Phase bipolar & Unipolar SPWM Inverter”** published in International Journal for Science and Advance Research in Technology , Volume : 4, Issue : 1, January 2018 (ISSN: 2395-1052).
2. Suraj Shete, Girish Bhide, Mahesh Jadhav, **“WHT and Double WHT: An effective PAPR reduction approach in OFDM” in IEEE Xplore Digital Library DOI:10.1109/ICAECCT.2016.7942577**
3. Paper titled **“ A Comparative Analysis of Adaptive Contrast Enhancement Equalization Techniques”** published in ASIAN JOURNAL OF CONVERGENCE IN TECHNOLOGY (AJCT ISSN : 2350 - 1146).
4. Paper titled **“Edge – Coupled DGS Composite Bandpass Filter for Wi-Max Application”** published in International Journal of Modern Trends in Engineering and Research, volume. 2, Issue (11), November 2015, pp. 109-113.

Papers Presented/Published in National/ International Conferences - 03

International Conferences – 01

1. Paper Presented titled **“WHT and Double WHT: An effective PAPR reduction approach in OFDM”** at 2016 IEEE International Conference on Advances in Electronics, Communication and Computer Technology (ICAECCT) Rajarshi Shahu College of Engineering, Pune India. Dec 2-3, 2016.

National Conferences – 02

1. Paper Presented titled **“ A Comparative Analysis of Adaptive Contrast Enhancement Equalization Techniques”** in national conference on Recent Advances in Science and Technology 18-19 Feb. 2016 at FAMT Ratnagiri.
2. Paper Presented titled **“Performance Evaluation of Single Phase bipolar & Unipolar SPWM Inverter”** in national conference on Changing Technology and Rural Development 22nd-23rd December 2017 at RMCET, Ambhav.

Number of Workshops, Seminars, STTPs, FDPs Attended – 12

STTPs and FDPs

1. Attended Two Week AICTE Sponsored **FDP on "Embedded System Design A Microcontroller Approach"** conducted by College of Engineering Pune from 20th November to 1st December 2017
2. Attended One Week AICTE Sponsored FDP on **"Image Processing and Computer Vision"** Organized by College of Engineering Pune from 12th Dec to 16th Dec 2016
3. Attended Four Week AICTE approved FDP by IIT Bombay on **"Use of ICT in Education for Online and Blended Learning"** from 2nd May to 10th July 2016
4. Attended Two Week STTP on **"MATLAB & SIMULINK: Learning by Doing"** under ISTE-TEQIP-II conducted by Government College Of Engineering Karad from 06th June to 18th June 2016.
5. Attended Two Week ISTE workshop on **"Basic Electronics"** Conducted by IIT Bombay under the National Mission on Education through ICT.

Conferences and Seminars

6. Attended one day seminar on **An Introduction to Speech Technology: Automatic Speech Recognition and Text –to-Speech Synthesis** organized by Department of E&TC FAMT, Ratnagiri on 31st January.
7. Attended 2nd **National Conference on Recent Advances in Science and Technology (RAST)** organized by Finolex Academy of Management and Technology, Ratnagiri from 17th to 18th February 2016

Two Week or One Week Workshops

8. Attended Two Days workshop on **"Introduction to Biomedical Imaging"** Organized by Department of Instrumentation and control Engineering, College of Engineering Pune
9. Attended **One Week Workshop on "Funding Schemes Available for Research Proposals"** Organized by Research Development and Consultancy cell (RDCC) of Finolex Academy of Management and Technology, Ratnagiri from 13th February to 23rd April 2016
10. Attended Two days ISTE workshop on **"Virtual Labs, Electronics"** Conducted by IIT Bombay and MHRD initiative.
11. Attended Two Days workshop on Simulation of **"Electronics Circuits & Power Electronics System using SEQUEL"** conducted by IIT Bombay

Workshops

12. Attended Three days workshop on 'Embedded Systems and Robotic Design' conducted by PPCRC, Pune.

Professional Membership

1. Indian Society of Technical Education (I.S.T.E)

College Exam Duties

1. Worked as a **Senior Supervisor** of **Engineering 2015 Winter Exam**.
2. Worked as a **Senior Supervisor** of **Engineering 2010 Winter Exam**.
3. Carried out exam related duties such as **Paper Setter, Moderator, Examiner and External Examiner** for conducting practical and oral exam as pointed by Board of Examination, Mumbai University.
4. Worked as a **Result -checking committee member**.
5. Worked a **scrutiny committee member**.
6. Worked as **an Invigilator for University theory exams**.

Other Experience

College Level Committees

1. Working as a **Departmental Website update coordinator** of **Website Administration committee** (WAC) of Finolex Academy of Management and Technology, Ratnagiri.
2. Working as **Departmental coordinator** of **Library Advisory committee** Of Finolex Academy of Management and Technology, Ratnagiri.
3. Worked as departmental organizing committee member in 2nd National Conference on **“Recent Advances in Science and Technology” (RAST-2K16)**.
4. Working as **Mentor** for 22 T.E students.
5. Working as **Class Teacher** for B.E Electronics.

Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience
1.	CMOS VLSI Design	B.E /ETRX / (Sem VIII)	2 Years
2.	Embedded System Design	B.E /ETRX / (Sem VII)	3 Years
3.	Design with Linear Integrated Circuits	T.E /ETRX / (Sem V)	1 Year
4.	Electronics Devices and Circuits	S.E /ELECT/ (Sem III)	2 Years
5.	Basic Electronics	S.E /ELECT/ (Sem III)	4 Years
6.	Electronics Circuit Design	S.E /ELECT/ (Sem IV)	4 Years
7.	Electronics Devices and Circuits I	S.E /E&TC/ (Sem III)	2 Years
8.	Electronics Devices and Circuits II	S.E /E&TC/ (Sem IV)	2 Years