

**Name** **MR. Sandeep R.Nalage.**  
**B.E. ELECTRONICS,**  
**M.E. Electronics & Telecommunication**

**Designation** Assistant Professor  
**Department** Electronics Engineering  
**Date of Joining the Institute** 23/04/2007  
**Education** B.E. Electronics, 2002  
M.E.Electronics & Telecommunication, 2015  
**Branch / Specialization** Electronics  
**Areas of Interest** Satellite communication, Embedded System Design, Electronics Devices and Circuits, Automation  
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### Academic Appointments

Name of the University / Institute / Industry	Duration	Designation
Finolex Academy of Management and Technology, Ratnagiri	From 02/05/2009 to till date	Assistant Professor
	From 23/04/2007 to 23/04/2009	Lecturer
<b>Total Experience – 10 Years</b>		

### Awards / Honors / Prizes

NIL

### Seminars/Lecture/Invited Talks Conducted

1. Delivered Seminar on "**Career planning with FAMT**" at Govt. Polytechnic Ratnagiri., Ratnagiri.
2. Delivered Seminar on "**Career planning with FAMT**" at Sahyadri Polytechnic Sawarde, Dist.Ratnagiri.
3. Delivered Seminar on "**Career planning with FAMT**" at Br. B. K. College, Vengurla.

4. Delivered Seminar on "**Career planning with FAMT**" at G. D. N. Jr College,Vettore,Venurla.
5. Delivered Seminar on "**Career planning with FAMT**" at Bhandari Jr. college,Malvan.
6. Delivered Seminar on "**Career planning with FAMT**" at B. M. Gogate Jr. College,Malvan.
7. Delivered Seminar on "**Career planning with FAMT**" at N. A. D. Topiwala Jr. College,Malvan

## Papers Published

### Papers Presented/Published in National/ International Journals - 08

#### International Journals – 02

1. SurajShete, Girish Bhide, Sandeep Nalage, Ashish Vartak, "**Composite Structure Fractal Wideband Monopole Antenna for ISM Band Applications**" in International Journal of Modern Trends in Engineering and Research (IJMTER) volume. 4, Issue (7), July 2017.
2. Sandeep R.Nalage,Mrs.M.R.Dixit. "**Low cost Portable Automatic Satellite Tracking System for DTH Antenna**" published in International Journal of Electrical, Electronics and Data Communication, 2013,Vol 1, Issue 7, 2013.

### Papers Presented/Published in National/ International Conferences - 01

#### International Conferences – 01

1. Paper presented titled "**Low cost Portable Automatic Satellite Tracking System For DTH Antenna**" International Conference on Electrical, Electronics and Communication Engineering (IACEECE) on 15<sup>th</sup> Sept.2013.at Pune.

### Number of Workshops, Seminars, STTPs, FDPs Attended – 11

#### STTPs and FDPs - 03

1. Attended One week ISTE STTP on "**Electric Power System**" at Finolex Academy of Management & Technology Ratnagiri, from 12<sup>th</sup> June to 15<sup>th</sup> July 2017.
2. Worked as coordinator for two week ISTE workshop on "**Basic Electronics**" Conducted by IIT Bombay from 28<sup>th</sup> June to 8<sup>th</sup> July 2011.
3. Attended five days training workshop for Coordinators "**Basic Electronics**" conducted by IIT Bombay from 1<sup>st</sup> to 5<sup>th</sup> March 2011.

### Conferences and Seminars - 01

1. Attended "**Low cost Portable Automatic Satellite Tracking System For DTH Antenna**" International Conference on Electrical, Electronics and Communication Engineering (IACEECE) on 15<sup>th</sup> Sept.2013.at Pune.

### Two Week or One Week Workshops - 02

1. Attended total one week workshop on weekends on "**Funding schemes Available for Research Proposals**" organized by RDCC, Finolex Academy of Management and Technology, Ratnagiri from 13<sup>th</sup> February to 23<sup>rd</sup> April 2016.
2. Attended One Week "**Yoga Course**" conducted by FAMT, Ratnagiri, from 17-22 June 2015.

### Workshops - 05

1. Attended Two day workshop on "**RF and Antennas – from Theory to Practice**", organized by Department of Electronics and Telecommunication Engineering, Finolex Academy of Management and Technology, Ratnagiri, on 11-12 August 2017.
2. Worked as coordinator Two day workshop on "**Mobile Antenna - Design, Fabrication and Testing**", at Finolex Academy of Management and Technology, Ratnagiri, on 01-02 August 2015.
3. Attended one day workshop on "**Introduction to La-tex**" organized by Department of Electronics Engineering KIT's college of Engineering on 5<sup>th</sup> may 2012
4. Attended a Two Day Workshop on "**Virtual Labs, Electronics**"conducted by IIT, Bombay, in Association with MHRD (Govt. of India) at Finolex Academy of Management and Technology, Ratnagiri on 09<sup>th</sup>and 10<sup>th</sup> April 2011.
5. Two day workshop on "**Simulation of Electronics Circuits & Power Electronics Systems using SEQUEL**", at Finolex Academy of Management and Technology, Ratnagiri 31 October and 01 November 2009.

### Professional Membership

1. Indian Society of Technical Education (ISTE)

### College Exam Duties

1. Worked as an **CAP coordinator**at FAMT for the Second Half of 2013examinations conducted by University of Mumbai
2. Worked as a **Senior Supervisor** of Engg. and **Second Half of 2009 Examinations** for the **Afternoon Session.**
3. Carried out exam related duties such as **Paper Setter, Moderator,for setting theorypapers as appointed by Board of Examination, Mumbai University.**
4. Worked as **External Examiner** For Electronics/Electrical/Electronics and Telecommunication Engg. Examinations conducted by university of Mumbai.

## Other Experience

### College Level Committees

1. Working as a core committee member of **NAAC Criteria – III** for Finolex Academy of Management and Technology, Ratnagiri.
2. Working as a Member of '**Admission Counseling Cell**'. Delivered a seminar on "Career Planning in Engineering with FAMT" to various Diploma colleges.  
Working as a **Member of Discipline committee** of Finolex Academy of Management and Technology, Ratnagiri.

### Judge Panel / Staff Selection Committee / Subject Expert

Nil

### Projects / Software Development

Nil

## Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience (no. of times taught)
1	Mobile Communication	B.E./ETRX/VIII	1
2	Discrete Electronic Circuits	S.E./ETRX/IV	4
3	Electronic Devices	S.E./ETRX/III	4
4	Electronic Devices and circuits	S.E./ELEC/III	3
5	Design with Linear Integrated Circuits	T.E./ETRX/V	2
6	Electronic Devices and Circuits-I	S.E./EXTC/III	2
7	Electronic Devices and Circuits-II	S.E./EXTC/III	2
8	Analog Electronics-I	S.E./EXTC/III	2
9	Analog Electronics-II	S.E./EXTC/IV	2
10	Electronic Circuit Analysis And Design	S.E./ETRX/IV	2

11	Basic Electronic Circuits	S.E./ETRX/III	2
12	Electronic Circuit Design	S.E./ELEC/IV	2
13	Basic Electronics	S.E./ELEC/III	2
14	Communication System	T.E./ETRX/V	1