

**Name** MR. AMOL R. SUTAR  
ME Electronics

Designation Assistant Professor  
Department Electronics Engineering  
Date of Joining the Institute 01/08/2006  
Education BE Electronics, ME Electronics  
Branch / Specialization Electronics Engineering  
Areas of Interest Embedded Systems, Power Electronics  
Email amol.sutar@famt.ac.in  
Personal Homepage link <https://wamanrparulekar.wordpress.com/>



### Academic Appointments

Name of the University / Institute / Industry	Duration	Designation
<b>Finolex Academy of Management and Technology, Ratnagiri</b>	<b>From 01/08/06 to till date</b>	<b>Assistant Professor</b>
<b>S. N. Systems, Pune</b>	<b>From 01/09/2005 to 30/07/2006</b>	<b>Electronics Engineer</b>

**Total Experience – 12 Years**

**Awards / Honors / Prizes**

### Research projects

1. A Project Titled **An Intelligent Adjustable and Automatic Timer System**, Applied for Minor Research Grant to Mumbai University 2017-18.
2. Many Electronics based Projects Guided to SE, TE, BE Students of Electronics, Electrical, IT, Mechanical branches, FAMT, Ratnagiri.

### Seminars/Lecture/Invited Talks Conducted

1. Conducted two day Workshop on **"Embedded C Programming and Application Development"** as resource person on 04<sup>th</sup> to 7<sup>th</sup> August 2017 at FAMT. Ratnagiri.
2. Conducted two day Workshop on **"Programming with Arduino"** on 03<sup>rd</sup> and 4<sup>th</sup> March 2017 at Government Polytechnic, Malvan.

3. Organized two day workshop on **"Raspberry-Pi"** on 10<sup>th</sup> and 11<sup>th</sup> Feb 2017 at FAMT Ratnagiri.
4. Conducted three day workshop on **"Embedded C Programming and Application Development"** as resource person on 6<sup>th</sup> August to 8<sup>th</sup> August 2016 at FAMT Ratnagiri.
5. Conducted three day workshop on **"Microcontroller System Design, C Programming and Applications"** as resource person on 3<sup>rd</sup> Oct 2015 to 5<sup>th</sup> Oct 2015, at FAMT Ratanagiri.
6. Organized a workshop **"Robo Hack"** as Workshop Coordinator at FAMT Ratnagiri on date 28<sup>th</sup> August-29<sup>th</sup> August 2015.
7. Conducted a workshop **"Workshop on Robotics"**, as Resource Person at FAMT Ratnagiri on date 17<sup>th</sup> October 2014.
8. Conducted a workshop **"Workshop on Robotics"**, as Resource Person at FAMT Ratnagiri on date 17<sup>th</sup> August 2013.
9. Conducted a one day Workshop on **"PCB Design and Circuit Implementation"** as Resource person at FAMT Ratnagiri, on date 23<sup>rd</sup> July 2008.

### Papers Published

### Papers Presented/Published in National / International Journals - 08

#### International Journals – 08

1. Paper titled **"Design and Development of an Intelligent, Adjustable and Automatic Timer System"** published in International Journal of Science and Research Technology IJSRT, Vol 3, Issue 10, ISSN 2395-1052, Oct-2017.
2. Published a paper in international journal on **"Simple and Low Cost IC Tester for Analog and Digital labs"**, Asian journal of convergence in Technology, AJCT, Vol-II, Issue III, PP, Sept. 2016.
3. Published a paper in international journal on **"Implementation and Study of BLDC Motor Drive System"**, IJESRT, ISSN2277-9655, Vol. 5, Issue5, PP 57-64, May 2016.
4. Published a paper in international journal on **"Performance Analysis of Three Phase induction motor drives for Various PWM Control methods"**, IJSER, ISSN 2229-5518, Volume 6, Issue 8, PP 1-7, August 2015.
5. Published a paper in international journal on **"Design of Xilinx (8.1) FPGA Based Five Level (Multilevel) PWM Single Phase Inverter"**, IJES, ISSN 2319-1805 Volume 2, Issue 5, PP-78-84, May 2013
6. Published a paper in international journal on **"Advanced Three Phase PWM Inverter Control Using Microcontroller"**, IOSR-JEEE, ISSN 2278- 1676, Volume 5, Issue 2, PP 21-28, April 2013.
7. Published a paper in international journal on **"Pulse Width Modulation Implementation using FPGA and CPLD ICs"**, IJSER, ISSN 2229-5518, Volume 3, Issue 8, PP 1-5 August 2012.
8. Published a paper in international journal on **"Performance Analysis of Z-source Inverter Fed Induction Motor Drive"** IJSER, ISSN 2229- 5518, Volume 3, Issue 5, PP 1-5, May2012.

### Papers Presented/Published in National/ International Conferences - 03

#### International Conferences – 01

1. Paper presented titled **"EMBEDDED SYSTEM APPLICATIONS IN WIRELESS COMMUNICATION"** in international Conference On Emerging Trends in Embedded Systems and VLSI Technology, MIT, Alandi held on 21<sup>st</sup> Dec. 2009 to 23<sup>rd</sup> Dec. 2009

## National Conferences – 02

1. Paper Presented titled "**Performance Evaluation of Single Phase Unipolar and Bipolar PWM Inverter**", in national conference CTRD2K17 at RM CET Ambav, Held in Dec. 2017
2. Paper presented titled "**SIMPLE AND LOW COST IC TESTER FOR ANALOG AND DIGITAL LABS**" in national Conference RAST 2K16 held in Feb. 2016.

## Number of Workshops, Seminars, STTPs, FDPs Attended – 16

### STTPs and FDPs

1. Attended Faculty Development Program on "**Embedded Systems**" on 14<sup>th</sup> Nov. to 18<sup>th</sup> Nov 2016 at IIT Pune
2. Attended Faculty Development Program on "**Digital Marketing**" on 16<sup>th</sup> Oct. 2016 at FAMT Ratnagiri.
3. Attended one day Orientation Program on "**Power Electronics-II**" at Shah and Anchor Cutchinii COE1 on date 30 June 2015.
4. Attended Two Week ISTE workshop on "**Analog Electronics (MHRD)**" Conducted by IIT Bombay under the National Mission on Education through ICT, on June 4 - June 14, 2013.

### Conferences and Seminars

5. Attended 2<sup>nd</sup> **National Conference on Recent Advances in Science and Technology (RAST)** organized by Finolex Academy of Management and Technology, Ratnagiri from 17<sup>th</sup> to 18<sup>th</sup> February 2016
6. Attended three day **National Conference On Advances in Heat and Mass Transfer, FAMT Ratnagiri**, held on 30<sup>th</sup> and 31<sup>st</sup> October 2009.
7. Attended three day **International Conference On Emerging Trends in Embedded Systems and VLSI Technology**, MIT, Alandi held on 21<sup>st</sup> Dec. 2009 to 23<sup>rd</sup> Dec. 2009

### Two Week or One Week Workshops

8. Attended two week **STTP Workshop on "Power System Analysis"** at FAMT Ratnagiri I association with IIT Kharagpur. July 2017
9. Conducted **One Week Workshop on "Funding Schemes Available for Research Proposals"** Organized by Research Development and Consultancy cell (RDCC) of Finolex Academy of Management and Technology, Ratnagiri from 13<sup>th</sup> February to 23<sup>rd</sup> April 2016
10. Attended **Two Week ISTE workshop on "Signals and Systems"** Conducted by IIT Bombay, at FAMT Ratnagiri on 2 Jan. 2014 to 12 Jan. 2014.
11. Attended Two Day workshop on "**Introduction to Robotics**" Organized by Department of Physics Mumbai University on date 30<sup>th</sup> Nov. 2012 & 1<sup>st</sup> Dec. 2012
12. Attended **one Week Workshop on "International workshop on low cost wireless computer networking"** conducted by Electronics Department IIIT Pune on date January 11 to January 15, 2010.

13. Attended **3 week** Workshop on "**Embedded Using Linux ARM & keil**" Organized by Electronics Department, Finolex Academy of Management and Technology, Ratnagiri. March 2007.

#### **Workshops**

14. Attended 2 Day **ISTE Workshop** for Teachers on "**Aakash for Education**" Conducted by IIT Bombay under the National Mission on Education through ICT from 10<sup>th</sup> to 11<sup>th</sup> November 2012.
15. Attended one day workshop on "**Total Quality Management in Technical Education**" organized by ISTE local chapter FAMT, Ratnagiri on 24<sup>th</sup> January 2009
16. Attended Two Day workshop on "**Simulation of electronic circuits & Power Electronics systems using SEQUEL**" Conducted by IIT Bombay at FAMT Ratnagiri on 31<sup>st</sup> Oct. 2009 to 1<sup>st</sup> Nov.2009.

#### **Professional Membership**

1. Indian Society of Technical Education (I.S.T.E)

#### **College Exam Duties**

1. Worked as a **Senior Supervisor** of **Engineering and MCA 2014 Summer Exam**.
2. Carried out exam related duties such as **Moderator, Examiner and External Examiner** for conducting theory, practical and oral exam as appointed by Board of Examination, Mumbai University.
3. Worked as departmental Exam In-charge of unit tests 2012-13.

#### **Other Experience**

##### **College Level Committees**

1. Working as a TNP Departmental Coordinator of Electronics Department, Finolex Academy of Management and Technology, Ratnagiri.
2. Working as a **Member** of R&D Committee, Finolex Academy of Management and Technology, Ratnagiri.
3. Worked as faculty in-charge of ESTA Committee.
4. Worked as departmental organizing **committee member** in 2nd National Conference on "**Recent Advances in Science and Technology**"
5. Worked as a Coordinator of **Avishkar 2016-17 for college level round and District level Round**, conducted successfully.
6. Working as a department coordinator and member for **NAAC Criteria 3 committee**

##### **Judge Panel / Staff Selection Committee / Subject Expert**

1. Worked as a **Judge** for intradepartmental project competition 2017-18 organized by Electronics and telecommunication Department, FAMT Ratnagiri.
2. Worked as a **Judge** for **state Level Technical Paper Presentation competition in Electrfocus2k17** organized by **Electronics Department, FAMT Ratnagiri**
3. Worked as a **Judge** for intradepartmental project competition 2016-17 organized by Electronics and telecommunication Department, FAMT Ratnagiri.

### Projects / Software Development

1. Designed and developed an **Automatic Alarm System** for Institute, Working successfully.
2. Designed and Developed Experimental kit of **Three Phase Inverter System** for conducting practical of Power Electronics subject.

### Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience
1.	Power Electronics - II	B.E. Electronics (Sem - VII)	5 Year
2.	Power Electronics - I	T.E. Electronics (Sem - VI)	5 Years
3.	Embedded Systems and Real Time Programming	B.E. Electronics (Sem - VIII)	2 Years
4.	Electromagnetic Fields and Waves	T.E. Electrical (Sem - V)	4 Years
5.	Principals of Communication Engineering	SE Electrical (Sem - IV)	2 Years
6.	Wireless Communication	B.E. Electronics (Sem - VII)	2 Years
7.	Electronics Devices and circuits	S. E. IT (Sem - III)	4 Years
8.	Electrical Network analysis and Synthesis	S.E. Electronics (Sem - III)	2 Years