

Name**MR. AMOL R. SUTAR**
ME (Electronics)

Date of Birth

13/09/1983

Highest
Qualification
Branch /
Specialization
DesignationMaster of Electronics Engineering
Embedded Systems, Power
Electronics.
Assistant Professor

Department

Electronics

Date of Joining the
Institute

01/08/2006

**Work Experience**

- Worked as an R & D Engineer in S. N. Systems, Pune for one year from 01/09/2005 to 30/07/2006.
- Working with Finolex academy of management and technology as Assistant Professor from 01/08/2006 to till date

(9 years experience)**Number of Papers
Presented/Published
in National/
International
Conferences
Details****1**

1. Paper presented titled "EMBEDDED SYSTEM APPLICATIONS IN WIRELESS COMMUNICATION" in international Conference On Emerging Trends in Embedded Systems and VLSI Technology, MIT., Alandi held on 21st Dec. 2009 to 23rd Dec. 2009

**Number of Papers
Presented/Published
in National/
International
Journals
Details****5**

1. Published a paper in international journal on "Performance Analysis of Three Phase induction motor drives for Various PWM Control methods", IJSER, ISSN 2229-5518, Volume 6, Issue 8, PP 1-7, August 2015.
2. Published a paper in international journal on." Design of Xilinx (8.1) FPGA Based Five Level (Multilevel) PWM Single Phase Inverter", IJES, ISSN 2319-1805 Volume 2, Issue 5, PP-78-84, May 2013

3. Published a paper in international journal on "Advanced Three Phase PWM Inverter Control Using Microcontroller", IOSR-JEEE, ISSN 2278-1676, Volume 5, Issue 2, PP 21-28, April 2013.
4. Published a paper in international journal on "Pulse Width Modulation Implementation using FPGA and CPLD ICs", IJSER, ISSN 2229-5518, Volume 3, Issue 8, PP 1-5 August 2012.
5. Published a paper in international journal on "Performance Analysis of Z-source Inverter Fed Induction Motor Drive" IJSER, ISSN 2229-5518, Volume 3, Issue 5, PP 1-5, May 2012.

Number of Workshops, STTPs Attended

9

Details

1. Attended one day Orientation Program on Power Electronics-II at Shah and Anchor cutchini COE1 on date 30 June 2015.
2. Attended Two Week ISTE workshop on 'Signals and Systems' Conducted by IIT Bombay, at FAMT Ratnagiri on 2 Jan. 2014 to 12 Jan. 2014.
3. Attended Two Week ISTE workshop on 'Analog Electronics (MHRD)' Conducted by IIT Bombay under the National Mission on Education through ICT, on June 4 - June 14, 2013.
4. Attended Two Day workshop on 'Introduction to Robotics' Organized by Department of Physics Mumbai University on date 30th Nov. 2012 & 1st Dec.
5. Attended 2 Day ISTE Workshop for Teachers on "Aakash for Education" Conducted by IIT Bombay under the National Mission on Education through ICT, on November 10-11, 2012.
6. Attended Two Day workshop on 'Simulation of electronic circuits & Power Electronics systems using SEQUEL' Conducted by IIT Bombay at FAMT Ratnagiri on date 31st Oct. 2009 to 1st Nov.
7. Attended 1 Day ISTE Workshop for Teachers on "Quality Education In Engineering Technical Institution" Conducted at FAMT Ratnagiri on date 24th Jan. 2009
8. Attended 1 Week Workshop on "International workshop on low cost wireless computer networking" conducted by Electronics Department IIIT Pune on date January 11 to January 15, 2010.

9. Attended 3 week Workshop on "Embedded Using Linux ARM & keil" Organized by Electronics Department, Finolex Academy of Management and Technology, Ratnagiri.

Details of student projects Guided at Masters Level	Nil
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Membership in Professional Bodies	1. Indian Society of Technical Education (I.S.T.E)
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Seminars/Lecture/Invited Talks Delivered As Resource Person	4
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| Details | <ol style="list-style-type: none">1. Conducted a workshop "Robo Hack" as Workshop Coordinator at FAMT Ratnagiri on date 28th August-29th August 2015.2. Conducted a workshop "Workshop on Robotics", as Rsource Person at FAMT Ratnagiri on date 17th October 2014.3. Conducted a workshop "Workshop on Robotics", as Rsource Person at FAMT Ratnagiri on date 17th August 2013.4. Conducted a one day Workshop on "PCB Design and Circuit Implementation" as Resource person at FAMT Ratnagiri, on date 23rd july 2008. |
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Awards / Honours / Prizes	NIL
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Research / Sponsored / Consultancy Projects	NIL
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