

Name **MR. AMOL R. SUTAR**
ME Electronics

Designation Assistant Professor
Department Electronics Engineering
Date of Joining the Institute 01/08/2006
Education BE Electronics, ME Electronics
Branch / Specialization Electronics Engineering
Areas of Interest Embedded Systems, Power Electronics
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Academic Appointments

Name of the University / Institute/ Industry	Duration	Designation
Finolex Academy of Management Technology, Ratnagiri	and From 01/08/06 to till date	Assistant Professor
S. N. Systems, Pune	From 01/09/2005 to 30/07/2006	Electronics Engineer

Total Experience – 13 Years

Awards / Honors / Prizes

1. Elite Certificate in eight week NPTEL Online Certification course on "Analog Circuits" with 82% score.

Research projects

1. A Project Titled **An Intelligent Adjustable and Automatic Timer System**, Applied for Minor Research Grant to Mumbai University 2017-18.
2. Many Electronics based Projects Guided to SE, TE, BE Students of Electronics, Electrical, IT, Mechanical branches, FAMT, Ratnagiri.

Seminars/Lecture/Invited Talks Conducted

1. Worked as member of organizing committee member for one day workshop on "Intellectual Property Rights" on 24th March 2018 at FAMT Ratnagiri organized by RDCC committee.

2. Conducted two day Workshop on **"Embedded C Programming and Application Development"** as resource person on 04th to 7th August 2017 at FAMT. Ratnagiri.
3. Conducted two day Workshop on **"Programming with Arduino"** on 03rd and 4th March 2017 at Government Polytechnic, Malvan.
4. Organized two day workshop on **"Raspberry-Pi"** on 10th and 11th Feb 2017 at FAMT Ratnagiri.
5. Conducted three day workshop on **"Embedded C Programming and Application Development"** as resource person on 6th August to 8th August 2016 at FAMT Ratnagiri.
6. Conducted three day workshop on **"Microcontroller System Design, C Programming and Applications"** as resource person on 3rd Oct 2015 to 5th Oct 2015, at FAMT Ratanagiri.
7. Organized a workshop **"Robo Hack"** as Workshop Coordinator at FAMT Ratnagiri on date 28th August-29th August 2015.
8. Conducted a workshop **"Workshop on Robotics"**, as Resource Person at FAMT Ratnagiri on date 17th October 2014.
9. Conducted a workshop **"Workshop on Robotics"**, as Resource Person at FAMT Ratnagiri on date 17th August 2013.
10. Conducted a one day Workshop on **"PCB Design and Circuit Implementation"** as Resource person at FAMT Ratnagiri, on date 23rd July 2008.

Papers Published

Papers Presented/Published in National / International Journals - 08

International Journals – 08

1. Paper titled **"Design and Development of an Intelligent, Adjustable and Automatic Timer System"** published in International Journal of Science and Research Technology IJSRT, Vol 3, Issue 10, ISSN 2395-1052, Oct-2017.
2. Published a paper in international journal on **"Simple and Low Cost IC Tester for Analog and Digital labs"**, Asian journal of convergence in Technology, AJCT, Vol-II, Issue III, PP, Sept. 2016.
3. Published a paper in international journal on **"Implementation and Study of BLDC Motor Drive System"**, IJESRT, ISSN2277-9655, Vol. 5, Issue5, PP 57-64, May 2016.
4. Published a paper in international journal on **"Performance Analysis of Three Phase induction motor drives for Various PWM Control methods"**, IJSER, ISSN 2229-5518, Volume 6, Issue 8, PP 1-7, August 2015.
5. Published a paper in international journal on **"Design of Xilinx (8.1) FPGA Based Five Level (Multilevel) PWM Single Phase Inverter"**, IJES, ISSN 2319-1805 Volume 2, Issue 5, PP-78-84, May 2013
6. Published a paper in international journal on **"Advanced Three Phase PWM Inverter Control Using Microcontroller"**, IOSR-JEEE, ISSN 2278- 1676, Volume 5, Issue 2, PP 21-28, April 2013.
7. Published a paper in international journal on **"Pulse Width Modulation Implementation using FPGA and CPLD ICs"**, IJSER, ISSN 2229-5518, Volume 3, Issue 8, PP 1-5 August 2012.
8. Published a paper in international journal on **"Performance Analysis of Z-source Inverter Fed Induction Motor Drive"** IJSER, ISSN 2229- 5518, Volume 3, Issue 5, PP 1-5, May2012.

Papers Presented/Published in National/ International Conferences - 03

International Conferences – 01

1. Paper presented titled **"EMBEDDED SYSTEM APPLICATIONS IN WIRELESS COMMUNICATION"** in international Conference On Emerging Trends in Embedded Systems and VLSI Technology, MIT, Alandi held on 21st Dec. 2009 to 23rd Dec. 2009

National Conferences – 02

1. Paper Presented titled **"Performance Evaluation of Single Phase Unipolar and Bipolar PWM Inverter"**, in national conference Challenging Technology and Rural Development (CTRD2K17) at RMCET Ambav, Held on 22rd and 23th Dec. 2017
2. Paper presented titled **"SIMPLE AND LOW COST IC TESTER FOR ANALOG AND DIGITAL LABS"** in national Conference RAST 2K16 held in Feb. 2016.

Number of Workshops, Seminars, STTPs, FDPs Attended – 16

STTPs and FDPs

1. Participated in eight week NPTEL online course on **"Analog Circuits"** (secured 82% score in online exam) in the month Feb-March 2018.
2. Attended Faculty Development Program on **"Emebdedd Systems"** on 14th Nov. to 18th Nov 2016 at I2IT Pune
3. Attended Faculty Development Program on **"Digital Marketing"** on 16th Oct. 2016 at FAMT Ratnagiri.
4. Attended one day Orientation Program on **"Power Electronics-II"** at Shah and Anchor Cutchinii COE1 on date 30 June 2015.
5. Attended Two Week ISTE workshop on **"Analog Electronics (MHRD)"** Conducted by IIT Bombay under the National Mission on Education through ICT, on June 4 - June 14, 2013.

Conferences and Seminars

6. Attended 2nd **National Conference on Recent Advances in Science and Technology (RAST)** organized by Finolex Academy of Management and Technology, Ratnagiri from 17th to 18th February 2016
7. Attended three day **National Conference On Advances in Heat and Mass Transfer, FAMT Ratnagiri**, held on 30th and 31st October 2009.
8. Attended three day **International Conference On Emerging Trends in Embedded Systems and VLSI Technology**, MIT, Alandi held on 21st Dec. 2009 to 23rd Dec. 2009

Two Week or One Week Workshops

9. Attended two week **STTP Workshop on "Power System Analysis"** at FAMT Ratnagiri I association with IIT Kharagpur. 12th June to 15th July 2017
10. Conducted **One Week Workshop on "Funding Schemes Available for Research Proposals"** Organized by Research Development and Consultancy cell (RDCC) of Finolex Academy of Management and Technology, Ratnagiri from 13th February to 23rd April 2016
11. Attended **Two Week ISTE workshop on "Signals and Systems"** Conducted by IIT Bombay, at FAMT Ratnagiri on 2 Jan. 2014 to 12 Jan. 2014.
12. Attended Two Day workshop on **"Introduction to Robotics"** Organized by Department

of Physics Mumbai University on date 30th Nov. 2012 & 1st Dec.2012

13. Attended **one Week** Workshop on "**International workshop on low cost wireless computer networking**" conducted by Electronics Department IIIT Pune on date January 11 to January 15, 2010.
14. Attended **3 week** Workshop on "**Embedded Using Linux ARM & keil**" Organized by Electronics Department, Finolex Academy of Management and Technology, Ratnagiri. March 2007.

Workshops

15. Attended 2 Day **ISTE Workshop** for Teachers on "**Aakash for Education**" Conducted by IIT Bombay under the National Mission on Education through ICT from 10th to 11th November 2012.
16. Attended one day workshop on "**Total Quality Management in Technical Education**" organized by ISTE local chapter FAMT, Ratnagiri on 24th January 2009
17. Attended Two Day workshop on "**Simulation of electronic circuits & Power Electronics systems using SEQUEL**" Conducted by IIT Bombay at FAMT Ratnagiri on 31st Oct. 2009 to 1st Nov.2009.

Professional Membership

1. Indian Society of Technical Education (I.S.T.E)

College Exam Duties

1. Worked as a **Senior Supervisor** of **Engineering and MCA 2014 Summer Exam**.
2. Carried out exam related duties such as **Moderator, Examiner and External Examiner** for conducting theory, practical and oral exam as appointed by Board of Examination, Mumbai University.
3. Worked as departmental Exam In-charge of unit tests 2012-13.

Other Experience

College Level Committees

1. Working as a TNP Departmental Coordinator of Electronics Department, Finolex Academy of Management and Technology, Ratnagiri.
2. Working as a Departmental R&D Coordinator, Finolex Academy of Management and Technology, Ratnagiri.
3. Worked as faculty in-charge of ESTA Committee.
4. Worked as departmental organizing **committee member** in 2nd National Conference on "**Recent Advances in Science and Technology**"
5. Worked as a Coordinator of **Avishkar 2016-17 for college level round and District level Round**, conducted successfully.

6. Working as a department coordinator and member for **NAAC Criteria 3 committee**

Judge Panel / Staff Selection Committee / Subject Expert

1. Worked as a **Judge** for intradepartmental project competition 2017-18 organized by Electronics and telecommunication Department, FAMT Ratnagiri.
2. Worked as a **Judge** for **state Level Technical Paper Presentation competition in Electrfocus2k17** organized by **Electronics Department, FAMT Ratnagiri**
3. Worked as a **Judge** for intradepartmental project competition 2016-17 organized by Electronics and telecommunication Department, FAMT Ratnagiri.

Projects / Software Development

1. Participated in **Science Project Exhibition** held on 7 to 9th December 2017 in Pparshuram Education Society's S. D. Gadre English Medium School, Chiplun.
2. Principal investigator of project titled "**An Intelligent, Adjustable and Automatic Timer System**" under minor research project scheme of Mumbai University 2017-18.
3. Designed and developed an **Automatic Alarm System** for Institute, Working successfully.
4. Designed and Developed Experimental kit of **Three Phase Inverter System** for conducting practical of Power Electronics subject.

Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience
1.	Power Electronics – II	B.E. Electronics (Sem - VII)	5 Year
2.	Power Electronics – I	T.E. Electronics (Sem - VI)	5 Years
3.	Embedded Systems and Real Time Programming	B.E. Electronics (Sem - VIII)	2 Years
4.	Electromagnetic Fields and Waves	T.E. Electrical (Sem - V)	4 Years
5.	Principals of Communication Engineering	SE Electrical (Sem - IV)	2 Years
6.	Wireless Communication	B.E. Electronics (Sem - VII)	2 Years
7.	Electronics Devices and circuits	S. E. IT (Sem - III)	4 Years
8.	Electrical Network analysis and Synthesis	S.E. Electronics (Sem - III)	2 Years
9.	Electronics Devices and circuits	S. E. Electronics (Sem - III)	1 Years