

Name	Miss.VRISHALI V. NIMBALKAR ME (Microelectronic Systems)
Designation	Assistant Professor
Department	Electronics Engineering
Date of Joining the Institute	03/07/2000
Education	BE Electronics, KBP College, 1999 ME (Microelectronic Systems), IISc Bangalore, 2009
Branch / Specialization	Microelectronics System
Areas of Interest	Digital and Analog VLSI Design, Microelectronics, Embedded System Design, Digital System Design
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Academic Appointments

Name of the University / Institute/ Industry	Duration	Designation
Finolex Academy of Management and Technology, Ratnagiri	From 03/07/2000 to till date	Assistant Professor
Government Polytechnic, Ratnagiri	From 09/08/1999 to 30/03/2000	Visiting Lecturer

Total Experience – 17 Years

Awards / Honors / Prizes

- Elite** certificate is awarded by **IIT Madras** for successfully completing **Basic Electrical Circuits NPTEL online course**, during July to September 2017, conducted by IIT Madras.
- Awarded** a cash prize of Rs.25000, by late **Shri. P.P. Chhabria**, Founder, Finolex Academy of Management and Technology for **presenting a paper in an International Conference titled "13th EUROMICRO Conference on Digital System Design: Architecture Methods and Tools"**, held in Lille, France on 1st to 3rd September 2010.

Seminars/Lecture/Invited Talks Conducted-09

1. As a resource person conducted a **four days** workshop on **“Embedded C Programming and Applications Development”**, on **04th to 07th August 2017**, at Finolex Academy of Management and Technology, organized by Electronics Engineering Department.
2. As a resource person conducted a **three days** workshop on **“Embedded C Programming and Applications Development”**, on **06th to 08th August 2016**, at Finolex Academy of Management and Technology, organized by Electronics Engineering Department.
3. As a resource person conducted seminars on **“Opportunities in Engineering Field and Career Guidance”** to 12th standard students of various junior science colleges of Ratnagiri, on **22nd, 23rd and 24th November 2016**.
4. As a resource person conducted a **three days** workshop titled **“Microcontroller System Design, C Programming and Applications”**, on **03rd to 5th October 2015**, at Finolex Academy of Management and Technology, organized by Electronics Engineering Department.
5. As a resource person conducted a **one day** workshop titled **“Workshop on Robotics”** on **04th October 2014**, at Finolex Academy of Management and Technology, organized by Electronics Engineering Department.
6. As a resource person conducted a **one day** workshop titled **“Workshop on Robotics”** on **17th August 2013**, at Finolex Academy of Management and Technology, organized by Electronics Engineering Department.
7. As a resource person conducted a **two days** workshop titled **“Interfacing with Microcontroller”** on **08th and 09th September 2012**, at Finolex Academy of Management and Technology, organized by Electronics Engineering Department.
8. As a resource person conducted a **one day** workshop titled **“Interfacing with Microcontroller”** on **15th September 2012**, at Finolex Academy of Management and Technology, organized by Electronics Engineering Department.
9. As a resource person organized a **one day** workshops titled **“Interfacing with Microcontroller”** on **08th September, 09th September, 01st October and 02nd October 2012**, at Finolex Academy of Management and Technology, organized by Electronics Engineering Department.

Papers Published

Papers Presented/Published in National/ International Journals - 01

International Journals – 01

1. Paper titled **“Partially Adaptive Cluster Based Routing for Network-on-Chip”** published in ASIAN JOURNAL OF CONVERGENCE IN TECHNOLOGY, Volume-II, Issue III, AJCT ISSN : 2350 - 1146.

Papers Presented/Published in National/ International Conferences - 02

International Conferences – 01

1. Paper Presented in an **International conference** titled **“In-Channel Flow Control Scheme for Network-on-Chip”** in 13th Euromicro Conference on Digital system Design: Architecture Methods and Tools, held on **1st to 3rd September 2010 in Lille, France**.

Paper Published in conference proceedings, by **IEEE Computer Society**, pp 459 to 466, **ISBN : 978-0-7695-4171-6**

National Conferences – 01

1. Paper presented titled **“Partially Adaptive Cluster Based Routing for Network-on-Chip”** at 2nd National Conference on Recent Advances in Science and Technology (RAST) organized by Finolex Academy of Management and Technology, on **17th and 18th February 2016**, Ratnagiri.

Paper Published in Conference proceedings **ISBN: 978-93-83046-31-7**, pp 142 to 146.

Number of Workshops, Seminars, STTPs, FDPs Attended – 30

STTPs and FDPs

1. Attended a **FDP** titled **“Self Dependence and Advantages of Technology & Digital Marketing”** organized by Lemures Technologies in association with Information Technology Students Association of Finolex Academy of Management and Technology, on **23rd September 2016**.
2. Participated in a **Four Week** AICTE approved **FDP** on **“Use of ICT in Education for Online and Blended Learning”** conducted by IIT Bombay, under the National Mission on Education through ICT, in association with MHRD, Govt. of India, from **2nd May 2016 to 10th July 2016**.
3. Participated in a **two week** ISTE **STTP** on **“Pedagogy for Effective use of ICT in Engineering Education”** Conducted by IIT Bombay under the National Mission on Education through ICT, in association with MHRD, Govt. of India, from **5th January 2015 to 31st January 2015**.

Conferences and Seminars

4. Attended 2nd **National Conference on Recent Advances in Science and Technology (RAST2k16)** organized by Finolex Academy of Management and Technology, Ratnagiri from **17th to 18th February 2016**.

5. Attended and participated in a **three days** international conference, titled "**13th Euromicro Conference on Digital System Design (DSD)**", on **1st to 3rd September 2010**.
6. Attended as well as, was a member of a **three days International conference** titled "**International Symposium on Low Power Electronics and Design**" organized by IISc, Bangalore, a centenary conference of IISc Bangalore, held on **25th May to 28th May 2008**.
7. Attended a **five days** conference titled "**Managing complexity in a distributed world (MCDES)**", a centenary conference organized by IISc Bangalore, at IISc Bangalore from **27th May to 31st May 2008**.

Two Week or One Week Workshops

8. Participated in a **two week** ISTE workshop on "**Introduction to Designs of Algorithms**" Conducted by IIT Kharagpur under the National Mission on Education through ICT, in association with MHRD, Govt. of India, from **27th April 2016 to 30th May 2016**.
9. Participated in a **one week** workshop on "**Funding Schemes Available for Research Proposals**" Organized by Research Development and Consultancy cell (RDCC) of Finolex Academy of Management and Technology, Ratnagiri, on **13th February 2016 to 23rd April 2016**.
10. Participated in a **two week** ISTE workshop on "**Signals and Systems**" conducted by IIT Kharagpur under the National Mission on Education through ICT, in association with MHRD, Govt. of India, from **2nd January to 12th January 2014**.
11. Participated in a **two week** ISTE workshop on "**Analog Electronics**" conducted by IIT Kharagpur, under the National Mission on Education through ICT, in association with MHRD, Govt. of India, from **4th June to 14th June 2013**.
12. Participated in a **two-week** ISTE Workshop on "**Introduction to Research Methodologies**" Conducted by IIT Bombay, under the National Mission on Education through ICT, in association with MHRD, Govt. of India, from **25th June 2012 to 04th July 2012**.
13. Participated in a **two-week** ISTE Workshop on "**Basic Electronics**", conducted by IIT Bombay, under the National Mission on Education through ICT, in association with MHRD, Govt. of India, from **28th June to 8th July 2011**.
14. Participated in a **fifteen days** workshop on "**Embedded Systems using Keil, Linux and ARM**" organized by Antech Technology foundation in association with Electronics Engineering Department, Finolex Academy of Management and Technology, on **03rd March to 17th March 2007**.

Workshops

15. Participated in a **two days** workshop on **“RF and Antennas-from Theory to Practice”** conducted by Dr. A. B. Kakade, RIT, Sakharale, and organized by Department of Electronics and Telecommunication Engineering, Finolex Academy of Management and Technology, on **11th and 12th August, 2017.**
16. Participated in a **two days** workshop on **“Raspberry Pi”**, conducted by Robokart in association with Innovation Cell IIT Bombay and Electronics Engineering Department, Finolex Academy of Management and Technology, on **10th to 11th FEB, 2017.**
17. Participated in a **three days** workshop on **“Embedded System and Robotic Design”** conducted by PPCRC, I2IT, Pune, in association with Electronics Engineering Department of Finolex Academy of Management and Technology, on **27th to 29th January, 2017.**
18. Participated in a **two days** workshop on **“Cisco Networking”**, conducted by Azure Skynet in association with Techkriti IIT Kanpur, and Association of Students of Electronics and Telecommunication of Finolex Academy of Management and Technology, on **3rd February to 04th February 2016.**
19. Participated a **two day** workshop on **“Mobile Antennas Fundamentals, Design, Fabrication and Testing”** conducted by Prof, A.B.Kakade, RIT Sakharale in association with Finolex Academy of Management and Technology, on **1st and 2nd August 2015.**
20. Participated in a **one day** orientation-cum-workshop on **“Inter-Collegiate Research Convection Avishkar 2014-15”** organized by Finolex Academy of Management and Technology on behalf of University of Mumbai, on **12th August 2014.**
21. Participated in a **one day** zonal workshop on **“Violence against Women”**, jointly organized by Gogate Joglekar College, Ratnagiri, and Women Development Cell, University of Mumbai on **06th August 2013.**
22. Participated in a **two days** workshop on **“Introduction to Robotics”** organized by MHRD sponsored e-yantra project of IIT Bombay in association with Department of Computer Science, University of Mumbai, on **30th November and 1st December 2012.**
23. Participated in a **two days** ISTE Workshop on **“Aakash for Education”** conducted by IIT Bombay under the National Mission on Education through ICT, in association with MHRD, Govt. of India, on **10th and 11th November 2012.**
24. Participated in a **two days** workshop on **“Virtual Labs, Electronics”** conducted by Electrical department of IIT Bombay and MHRD initiative, jointly organized by Department of Electronics Engineering and Electronics and Telecommunication Engineering, of Finolex Academy of Management and Technology, on **9th and 10th April 2011.**
25. Participated in a **two days** workshop on **“Haptics RoboARM”** conducted by ARK Technosolutions in association Greatstep IIT Kharagpur, and with Brainwaves 2011 of Finolex Academy of Management and Technology, on **26th and 27th February 2011.**
26. Participated in a **two days** workshop on **“simulation of Electronic Circuits and Power Electronic Systems using SEQUEL”** conducted by Prof. M.B.Patil, IIT Bombay in association with Finolex Academy of Management and Technology, on **31st October 2009 and 1st November 2009.**

27. Participated in a **three days** workshop on **“ASIC Design and CAD Tools for VLSI”** conducted and organized by I2IT, Pune, at I2IT Pune, on **15th to 17th February 2006**.
28. Participated in a **two days** workshop on **“LINUX”** conducted and organized by, Finolex Academy of Management and Technology, on **12th and 13th August 2006**.
29. Participated in a **one day** workshop on **“Advances in Micro-electronics”** conducted by eminent professors from the Department of Electrical Engineering, IIT Bombay, sponsored by IEEE AP/ED, Mumbai chapter, in association with Electronics Engineering Department, Finolex Academy of Management and Technology, on **15th March 2003**.
30. Attended a **one day** seminar on **“Self Reengineering”**, organized by Human Resource Development, Victory Associates in association with Finolex Academy of Management and Technology, on **06th January 2003**.

Professional Membership

1. Life member of The Indian Society for Technical Education (I.S.T.E), ID: LM 56164.

College Exam Duties

1. Worked as a **Senior Supervisor** for the **First Half of 2017 Engineering and MCA Examination**, held during **12th May to 15th June 2017**.
2. Was appointed as a **Joint Chief Conductor for First Half of 2013 Engineering Examination**, at RM CET, Ambav, from **27th May 2013 to 07th June 2013**.
3. Worked as a **Senior Supervisor** for the **First Half of 2011 Engineering and MCA Examination**, (Morning Session), held during **24th May to 20th June 2011**.
4. Worked as a **Senior Supervisor** for **Second Half of 2006 Engineering Examination** (Morning Session).
5. Worked as a **Senior Supervisor** for **Second Half of 2005 Engineering Examination** (Morning Session), held from **20th October 2005**.
6. Worked as a coordinator to conducted **Sessional Test-II** for Electronics Department from **17th October to 22nd October 2005**.
7. Worked as an **Additional Senior Supervisor** for **Second Half of 2001 Engineering Examination**, held from **03rd December 2001**.
8. Carried out exam related duties such as **Paper Setter, Moderator, External Examiner and Internal Examiner** for conducting practical and oral exam as appointed by Board of Examination, Mumbai University

Other Experience

College Level Committees

1. As a resource person participated in a **three day** taluka level science exhibition "**Disha (mission-2020)- A Science Exhibition**" held on **7th, 8th and 9th December 2017**, in Parshuram Education Society's S.D.Gadre English Medium School, Chiplun.
2. Co-ordinated a **three days** workshop on "**Embedded System and Robotic Design**" organized by PPCRC, I2IT Pune in association with Electronics Engineering Department, Finolex Academy of Management and Technology, on **27 to 29th January, 2017**.
3. Faculty member of an **Anti Ragging committee**.
4. Member of an **Anti-Ragging Squad committee**.
5. Institute representative of an **Internal Complaint Committee**.
6. Worked as a **coordinator** for a **three days** workshop on "**Embedded System and Robotic Design**" organized by PPCRC, I2IT Pune in association with Electronics Engineering Department of Finolex Academy of Management and Technology, on **27th to 29th January, 2017**.
7. Working as a **core** member of **Mentorship Committee**.
8. Member of **Alumni Committee**.
9. Member of **NAAC criteria 7 committee**.
10. Working as a **Teaching Staff Representative of WDC Committee** of Finolex Academy of Management and Technology (FAMT), from **01st July 2016 to till date**.
11. Worked as a **Chair person with Convener of Women Development Cell (WDC)** of Finolex Academy of Management and Technology, from **12th February 2013 to 12th February 2016**.
12. Worked as an **Organizing committee member**, for a two days national conference titled "**Recent Advances in Science and Technology (RAST2K16)**" held at Finolex Academy of Management and Technology, on **17th and 18th February 2016**.
13. Attended an awareness program titled "**Awareness program on Mammography**" held in Parkar Hospital, Ratnagiri on **03rd July 2016**.
14. Working as a member of **Admission Counseling Cell** from **27th October 2016 to 1st December 2016** and have delivered four seminars on "**Career Guidance**" to 12th standard science students of different science colleges in Ratnagiri.
15. Worked as a member of **Data verification and Data entry team** from **02nd June to 16th June 2016**.
16. Appointed as a **coordinator** to prepare academic time-table for Electronics Department for the year **2016-17**.

17. Attended seven days **Yoga** course organized by Finolex Academy of Management and Technology, on the occasion of International Yoga Day 2015, from **17th June 2015 to 22nd June 2015**.
18. Organized a seminar for all girls of FAMT titled "**Health and Hygiene**" on **10th September and 11th September 2014**.
19. Worked as an **Invigilator for GATE exam** on **15th February, 16th February and 01st March 2014**.
20. Worked for **four years** as a "**Faculty-in-charge of ESTA committee**" of Electronics Engineering department from **01st July 2010 to 30th June 2014**.
21. Participated in an event "**Samuhic Suryanamaskar**" organized by Madhurangan and Tarun Bharat, held at Police ground, Ratnagiri, on **17th February 2013**.
22. Organized a social event titled "**Information about Maher**" on **15th March 2013** at FAMT, Ratnagiri.
23. Organized a **five days** industrial visit for TE Electronics students, to **Karnataka Power Corporation Ltd.**, Yelasandra, Kolar District, Karnataka, from **24th February to 28th February 2012**.
24. Worked as a **coordinator** for a two days lecture series titled "**Fuzzy sets, Fuzzy Logic with Applications**", by Prof. Ashok Deshpande, on **25th and 26th February 2010**.
25. Organized an industrial visit to "**Finolexcabels and Dlink**", Goa from **23rd to 24th February 2004**.
26. Appointed as a **coordinator** to prepare academic time-table for Electronics Department for the year **2001-04**.

Judge Panel / Staff Selection Committee / Subject Expert

1. Worked as a judge for "**Project Exhibition and Competition**", organized by Electronics and Telecommunication Department of Finolex Academy of Management and Technology, on **14th July 2017**.

Subject Taught

Sr. No.	Name of the Subject	Year/Branch/Semester	Experience in semesters
1.	Integrated Circuit Technology	BE/ETRX/VII	3
2.	Basic VLSI Design	TE/ETRX/VI	3
3.	Advance VLSI Design	BE/ETRX/VIII	3
4.	VLSI Design	BE/ETRX/VII	3
5.	Embedded Systems and Real Time Programming	BE/ETRX/VIII	2
6.	Microcontroller and Applications	TE/ETRX/VI	3
7.	Microprocessor and Microcontrollers-I	TE/ETRX/V	4
8.	Microprocessor and Microcontrollers-II	TE/ETRX/VI	3
9.	Principles of Microprocessor Systems	SE/ETRX/IV	4
10.	Microprocessors	TE/IT/V	2
11.	Digital Design-I	SE/ETRX/III	4
12.	Digital Design-II	SE/ETRX/III	4
13.	Digital Electronics	SE/ELECT/III	4
14.	Digital Integrated Circuits	TE/ETRX/V	4
15.	Digital Logic Design and Applications	SE/IT/III	3
16.	Analog and Digital Integrated Circuits	SE/ELECT/VI	3
17.	Engineering Materials and Components	SE/ELECT/III	1