

Name **MR. GIRISH GOVIND BHIDE**
B.E. ELECTRONICS,
M.E.ELECTRONICS

Date of Birth 09/10/1972
Highest Qualification M.E.Electronics
Branch / Specialization Electronics
Designation Associate Professor and HoD
Department Electronics Engineering
Date of Joining the Institute 15/07/1996



Work Experience **Working with Finolex Academy of Management and Technology as Associate Professor from 15/07/1996 to till date (Total teaching Experience 21 Years)**

Number of Papers Presented/Published in National/ International Conferences **01**

Details

- 1. Paper presented titled "A Three-Phase Voltage Source Inverter Using MOSFET Devices And Equal Area Criteria PWM" at the international Conference on Global Technology Initiatives at Rizvi College of Engg. Held in March 2012.**

Number of Papers Presented/Published in National/ International Journals **01**

Details

- 1. Paper published titled "A Three-Phase Voltage Source Inverter Using MOSFET Devices And Equal Area Criteria PWM" Issue 1, No.1 (2012), pp – C29-C38**

Number of Workshops, STTPs Attended **20**

Details

- 1 Two day workshop on "Mobile Antenna - Design, Fabrication and Testing", at Finolex Academy of Management and Technology, in August 2015.**

- 2 Attended One Week STTP on "Assessment and Accreditation with NAAC for Quality Improvement of the Institute" at Saraswati College of Engg. Kharghar, Navi Mumbai from 06-10 July, 2015.**
- 3 Attended One Week "Yoga Course" conducted by FAMT, Ratnagiri, from 17-22 June 2015.**
- 4 Attended as a Teaching Assistant in Two Week ISTE workshop on "Control Systems" Conducted by IIT Kharagpur under the National Mission on Education through ICT at RC 1065 (FAMT) from 02 -12 December 2014.**
- 5 Attended 2 Day "RF Filter Design Workshop" conducted by IEEE-DBIT MTT-S Student Chapter on 13-14 September 2014.**
- 6 Attended Two Week ISTE workshop on "Computer Networking" Conducted by IIT Bombay under the National Mission on Education through ICT at RC 1065 (FAMT) from 30 June to 05 July 2014.**
- 7 Attended One week Co-ordinator's ISTE-IITKGP Workshop on "Analog Electronics" conducted by IIT, Kharagpur under NMEICT sponsored by MHRD from 01-05 April 2013.**
- 8 Attended One Day Training Program "Cisco Certified Network Associate" organized by ASENT, conducted by IP Solutions on 19 January 2013.**
- 9 Attended 2 Day ISTE Workshop for Teachers on "Aakash for Education" Conducted by IIT Bombay under the National Mission on Education through ICT on 10-11 November 2012.**
- 10 Attended as a Teaching Assistant in Two Week ISTE workshop on "Basic Electronics" Conducted by IIT Bombay under the National Mission on Education through ICT at RC 1065 (FAMT) from 28 June to 08 July 2011.**
- 11 Attended a Two Day workshop on "Virtual Labs, Electronics" conducted by IIT, Bombay in Association with MHRD, (Govt. of India) at Finolex Academy of Management and Technology, Ratnagiri on 9th and 10th April 2011.**
- 12 Attended One Week QIP Short Term Course on "Satellite Image Processing and Analysis" conducted by IIT, Bombay from 03-07 January 2011.**

- 13 Two day workshop on "Simulation of Electronics Circuits & Power Electronics Systems using SEQUEL", at Finolex Academy of Management and Technology, Ratnagiri 31 October and 01 November 2009.
- 14 Two day workshop on "Simulation of Electronics Circuits & Power Electronics Systems using SEQUEL", at Finolex Academy of Management and Technology, Ratnagiri 31 October and 01 November 2009.
- 15 Attended One week ISTE STTP on "Advanced Communication Systems" at VIIT, Pune from 23-28 June 2008.
- 16 Attended One Day Workshop on "Engineering Electromagnetics – Practical Approach" jointly organized by ISTE Chapter KJSCE and University of Mumbai, on September 01, 2007.
- 17 Attended Two Day CEP Course on "Wireless Networks" conducted by IIT Bombay from 27-28 November 2006.
- 18 Attended Two Day workshop on "Linux" at Finolex Academy of Management and Technology, Ratnagiri in August 2006.
- 19 Attended Two Week STTP on "Broadband Communication: Current Trends and Applications" jointly organized by University of Mumbai and ISTE Chapter KJSCE, from 19-30 June 2006.
- 20 One day workshop on "Advances in Micro-electronics", organized by IIT Mumbai at Finolex Academy of Management and Technology, Ratnagiri in March 2003.

Details of student projects Guided at Masters Level

NIL

Membership in Professional Bodies

1. Indian Society of Technical Education (I.S.T.E) (Life Member LM 29426)
2. Institute of Electrical and Electronics Engineers. (Member 92620161)

Seminars/Lecture/Invited Talks Delivered As Resource Person

01

Details

1. Delivered Lectures (02 Days) on the Subject "Electromagnetic Wave Theory and Propagation" – Sem IV , EXTC at Gharda Institute of Technology, Level

(Invitation by Prof. Munghate)

Awards / Honours / Prizes

01

- 1. Member of Governing Body of Finolex Academy of Management and Technology, Ratnagiri**

Research / Sponsored / Consultancy Projects

01

- 1. Developed the Examinations Result Preparation (Sem III to Sem VI) system based on Microsoft Excel for Rajaram Shinde College of Engineering, Pedhambe in the year 2010-11.**