MUMBAI UNIVERSITY FINOLEX ACADEMY OF MANAGEMENT AND TECHNOLOGY, RATNAGIRI

SUBJECT NAME>			1			2			3		4			5			6		7		
			CONTINUOUS TIME SIGNAL & SYSTEM			MICROPROCESSOR & MICROCONTROLLER - I			ELECTROMAGNE TIC ENGINEERING		LINEAR INTEGRATED CIRCUITS AND DESIGN			DIGITAL COMMUNICATION AND CODING TECHNIQUES			ENVIRONMENTAL		ELECTROINC S WORKSHOP	OF SEM	REMARKS
			Written	Term Work	Oral	Written	Term Work	Practical / Oral	Written	Term Work	Written	Term Work	Practical / Oral	Written	Term Work	Oral	Written	Term Work	Oral		
		Max Marks>	100	25	25	100	25	25	100	25	100	25	25	100	25	25	50	25	50	850	
		MIN Marks>	40	10	10	40	10	10	40	10	40	10	10	40	10	10	20	10	20		
SR.	EXAM																				
NO.	SEAT NO.	NAME OF THE CANDIDATE																			
1	5203	VADATE SAGAR BHASKAR USHA	51 +	13 +	15 +	40 +	10 +	14 +	<u>30</u> F	12 +	40 +	14 +	16 +	40 +	13 +	18 +	32 +	16 +	43 +	417	FAIL
2	5204	/ VISHWASRAO SHWETA VASANT SEEMA	51 +	10 +	13 +	58 +	14 +	20 +	<u>30</u> F	12 +	40 +	16 +	15 +	41 +	14 +	19 +	28 +	20 +	28 +	429	FAIL

REVALUATION RESULT OF THIRD YEAR ELECTRONICS ENGINEERING(SEM-V) OLD , MAY 2016 EXAMINATIONS

Marks after revaluation are indicated using underline.

#=0.229, @=0.5042, 0.5043, 0.5044, *=0.5045, +=Marks carried, /=Female, AB=Absent, --=Not applicable, E=Exemption can be claimed NULL & VOID=No marks in term work, PPR=Passed previously, X=Passed semester with exemption

Entered by:

Checked by:

Principal: