

UNIVERSITY OF MUMBAI
OFFICE REGISTER FOR THE B.E. (ELECTRONICS ENGINEERING) (SEM VII) (REVISED COURSE) EXAMINATION HELD IN NOVEMBER 2016
COLLEGE/CENTRE : 443 FAMT FEBRUARY 15, 2017

SEAT NO.	NAME OF CANDIDATE CENTRE	PP1			PP2			PP3			PP4			PP5			PP6		TOTAL	RESULT
		WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	WR	TW	O/P	TW	O/P		
		100	25	25	100	25	25	100	25	25	100	25	25	100	25	25	25	25	800	
		40	10	10	40	10	10	40	10	10	40	10	10	40	10	10	10	10		
01.	VLSI DESIGN																			
03.	POWER ELECTRONICS AND DRIVES																			
05.	ELECTIVE-I : 5:WIRELESS COMMUNICATION																			
50177043	GODBOLE MUKUL MILIND MANA LI	AA	16+	16+	AA	19+	16+	40+	14+	12+	43+	15+	18+	42+	13+	19+	17+	17+	317	A ABS
05.	ELECTIVE-I : 8:DIGITAL IMAGE PROCESSING DESIGN																			
50177044	SINGH GURJINDER JAIRAM JA SVIR	53	16+	20+	40+	16+	15+	41+	15+	18+	41+	20+	22+	40+	17+	18+	21+	20+	433	P
50177045	WAGHMARE VIPUL WAMAN VISH AKHA	29F	16+	17+	43+	16+	15+	53+	18+	17+	40+	18+	20+	41+	16+	22+	20+	20+	421	F

#:O.229; @:O.5042; *:O.5045;RCC:O.5050; +:MARKS CARRIED; /:FEMALE;NULL:NULL & VOID AS NO MARKS IN T.W.;P:PASSES;F:FAILS;A:ABSENT;
--:NOT APPLICABLE;E:EXEMPTION CAN BE CLAIMED; RR:RESERVED; ADC:ADMN. CANCELLED; PPR: PASSED PREVIOUSLY; RLE- LOWER EXAM NOT CLEAR;
~ : DYSLEXIA BENEFIT