## FINOLEX ACADEMY OF MANAGEMENT & TECHNOLOGY, RATNAGIRI DEPARTMENT OF ELECTRONICS AND TELECOMMUNICATION ENGG.

Date: 09/08/2017

## **NOTICE**

All the students of SE, TE and BE are hereby informed that the Time Table of Internal Assessment-I is as follows.

## TIME TABLE OF INTERNAL ASSESSMENT - I EXAMINATION

DAY & DATE	TIME	Sem III	Sem V	Sem VII
21/08/2017 Monday	10.00 am - 11.00 am	AM-III	MCA	IVP
	3.00 pm - 4.00 pm	EDC-I	AC	MOB. COM
22/08/2017 Tuesday	10.00 am - 11.00 am	DSD	RSA	OCN
	3.00 pm - 4.00 pm	CTN	RFMA	MRE
23/08/2017 Wednesday	10.00 am - 11.00 am	EIC	IC	ELECTIVE

## Note:

- 1. Paper will be of 20 marks
- 2. Students coming late will not be allowed for the examination.
- 3. Attendance is mandatory.