

Finolex Academy of Management & Technology, Ratnagiri
Department of Electronics Engineering
ON BEHALF OF UNIVERSITY OF MUMBAI
INTERNAL ASSESSMENT KT EXAMINATION FOR SEMESTER III to VIII (CBSGS)

Date-29/08/2017

Students having failure in **Internal Assessment** of semester III to VIII (CBSGS) are hereby informed that their **KT** examinations are scheduled as per the time table mentioned below. Since only one examination will be conducted for the Internal Assessment KT for 20 Marks, students having KT in these heads must appear for this examination.

Date	SEM III	SEM IV	SEM V	SEM VI	SEM VII	SEM VIII
11/09/17	AM-III	AM-IV	MCA	BVLSI-D	ICT	CMOS VLSI
12/09/17	ED	DEC	EME	AIS	PE-II	MEMS
13/09/17	DCD	M&P	SS	CO	ESD	ANT
14/09/17	CT	FCE	DLIC	PE-I	CCN	ELECTIVE-II
15/09/17	EIM	PCS	DC	DSP&P	ELECTIVE-I	---
16/09/17	---	EM	---	MITM	---	---

1. The time of examination will be **4:15 pm-5:15 pm** everyday.
2. Venue:- **EN-1/9 Lecture Hall**
3. **No repeat examination is possible under any circumstances.**
4. **In case, any student found overlapping of KT examinations of two or more subjects on same day. Student has to contact immediately to Prof.S.A.Shete.**
5. **Students are required to meet the concerned subject teachers for further details.**