

FINOLEX ACADEMY OF MANAGEMENT & TECHNOLOGY, RATNAGIRI
DEPARTMENT OF ELECTRONICS ENGG.

Date: 05/10/2017

NOTICE

All the students of SE, TE and BE are hereby informed that the Time Table of Internal Assessment-II (Second Half of 2017) is as follows.

TIME TABLE OF INTERNAL ASSESSMENT - II EXAMINATION

DAY & DATE	TIME	Sem III	Sem V	Sem VII
23/10/2017 Monday	3.00 pm -4.00 pm	AM-III	MCA	ESD
24/10/2017 Tuesday	10.00 am -11.00 am	EDC-I	DLIC	ICT
	3.00 pm -4.00 pm	DCD	EME	PE-II
25/10/2017 Wednesday	10.00 am -11.00 am	ENAS	SS	CCN
	3.00 pm -4.00 pm	EIM	DC	ELECTIVE I

Note:

1. Paper will be of 20 marks
2. **Students coming late will not be allowed for the examination.**
3. Attendance is mandatory.

HOD ETRX
(PROF.G.G.BHIDE)