**Finolex Academy of Management & Technology, Ratnagiri**

**Department of Electrical Engineering**

ON BEHALF OF UNIVERSITY OF MUMBAI

**INTERNAL ASSSESMENT – I EXAMINATION FOR SEMESTER III (Direct Second year students) (CBCGS)**

Date: 11/09/2017

Internal Assessment-I of Direct Admission to Second Year Electrical Engineering students (Sem-III) is scheduled on 21st Sept.2017 to 26th Sept. 2017. Time table for the same is mention below.

|  |  |
| --- | --- |
| **Date/Day** | **S E Electrical (Direct Second year Admission) SEM-III** |
| **Sub** | **Time** | **Venue** |
| 21/09/2017Thursday | AM-III | 4:15pm-5:15pm | EN-0/09 |
| 22/09/2017Friday | EDC | 4:15pm-5:15pm | EN-0/09 |
| 23/09/2017Saturday | CNPG | 4:15pm-5:15pm | EN-0/09 |
| 25/09/2017Monday | EMC-I | 4:15pm-5:15pm | EN-0/09 |
| 26/09/2017Tuesday | EEM | 4:15pm-5:15pm | EN-0/09 |

Note:

1. The time of examinations will be 4:15pm to 5:15pm every day.

2. Examination will be not conducted repeatedly under any circumstances.

3. Students are required to meet the concerned subject teachers for further details.

 **(Prof. S H Mhabadi)**

Head of the Department

 Department of Electrical Engineering

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