



HOPE Foundation's
**Finolex Academy of Management & Technology,
Ratnagiri**

Department of Electrical Engineering

**One-Day Workshop on PID Controller & PLC Simulator - Essentials
for Electrical Engineers**

A One Day Workshop on PID Controller & PLC Simulator- Essentials for Electrical Engineers was held on **02 March 2024** at Finolex Academy of Management & Technology (FAMT). Organized by FAMT's Electrical Engineering department, the workshop catered to diploma students from Govt. Polytechnic, Ratnagiri.

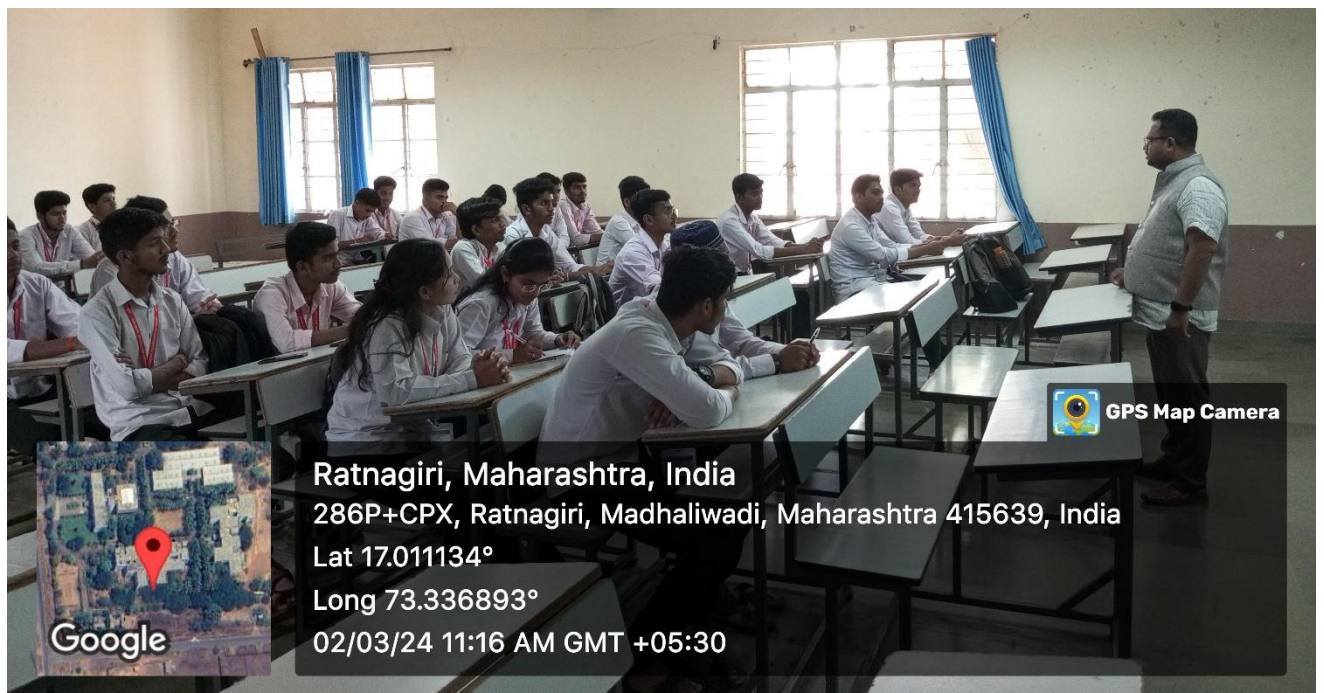
The workshop aimed to equip participants with a thorough understanding of vital tools and techniques in modern electrical engineering. In the inaugural session, **Dr. J. J. Mane**, the Head of Electrical Engineering Department, outlined the workshop's goals and its role in shaping the future of Electrical Engineers. He also discussed career prospects for aspiring engineers, emphasizing opportunities in renewable energy, automation, and beyond. The workshop commenced with **Prof. P. A. Potdar** leading a session to explore a selection of kits available in the electrical department, all designed to enhance learning experiences.

Following that, an in-depth session on PID controllers was conducted by **Prof. S. S. Wamane**, where participants were introduced to the principles and practical applications of PID control algorithms. Later on, **Prof. P. J. Kadam** engaged in hands-on activities simulating PLC operations, gaining proficiency in programming logical sequences and troubleshooting PLC-based systems. In the Python programming segment, **Prof. R. B. Ingle** introduced Python syntax, data structures, and libraries relevant to engineering applications. Practical exercises allowed attendees to apply Python for automation and data analysis.

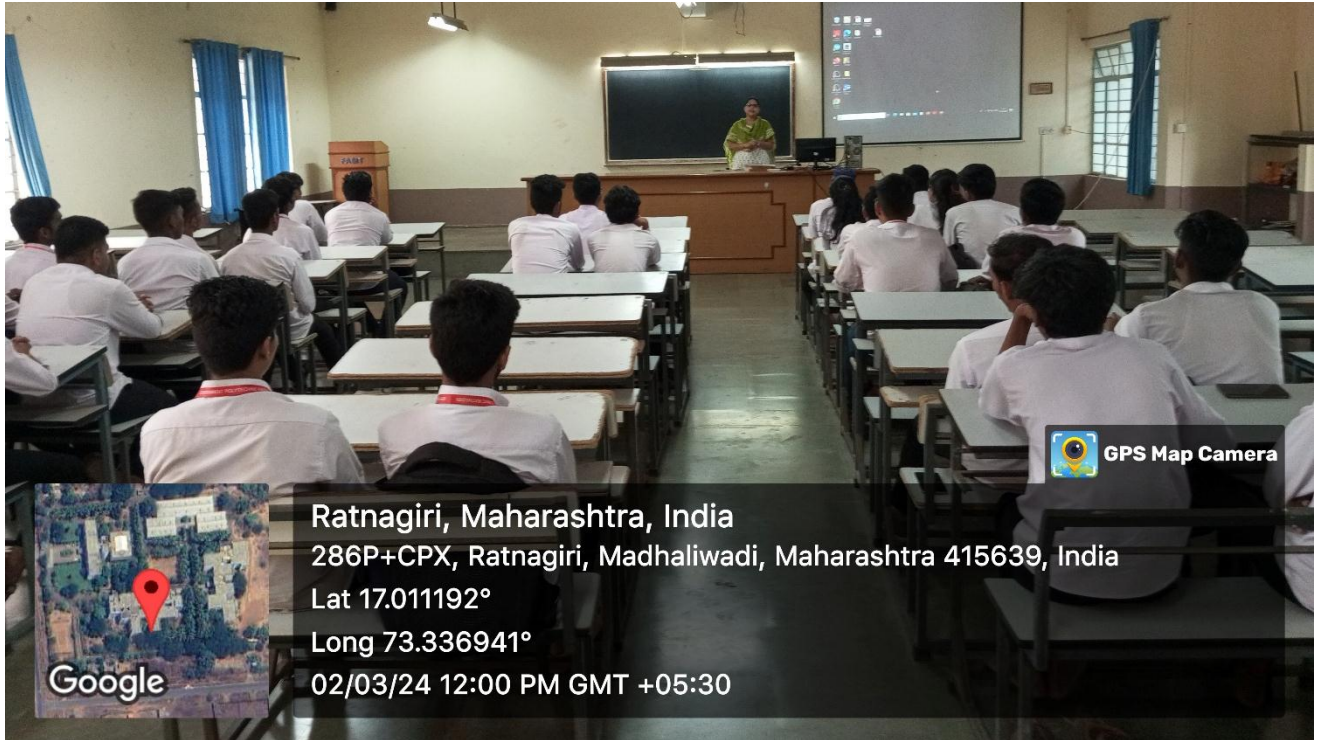
Concurrently, **Prof. D. Rajeshkumar** guided the participants through the basics of modelling, simulation, and analysis using MATLAB Simulink, demonstrating its utility in various engineering applications. **Prof. M. S. Modak** engaged insightful

session on Vedic Mathematics, where participants mastered mental calculation techniques and problem-solving strategies applicable to electrical engineering tasks. A total of **28** final-year students of Govt. Polytechnic, Ratnagiri, participated in this workshop. The coordination of the workshop was overseen by **Prof. S. S. Wamane, Prof. M. N. Tagare & Prof. S. H. Mhabadi** under the guidance of **Dr. Jayant J. Mane**, Head of the Electrical Engineering department.

Photos of the Event



Dr. J. J. Mane highlighting the boundless opportunities in Electrical Engineering



Prof. P. A. Potdar igniting curiosity and hands-on learning among workshop participants



Students engage in interactive sessions with Prof. M. S. Modak & Prof. P. J. Kadam



Recourse Persons along with participants from Govt. Polytechnic, Ratnagiri