



**HOPE Foundation's**  
**Finolex Academy of Management & Technology,**  
**Ratnagiri**  
**Department of Electronics and Telecommunication**  
**Engineering**

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## **Career Opportunities in Embedded Engineering**

An Online Alumni Session on '**Career Opportunities in Embedded Engineering**' was organized by the Electronics and Telecommunication Engineering department on 28<sup>th</sup> August 2021.

**Mr Danish Vasta**, Senior Firmware Engineer at Western Digital, Bengaluru and an alumnus of the EXTC 2016 batch delivered the session for T.E. and B.E EXTC students through Google Meet.

The main objectives of the Alumni interaction session were:

1. To create awareness about industry expectations (skill sets) for EXTC students
2. To explore Career opportunities in Embedded Engineering

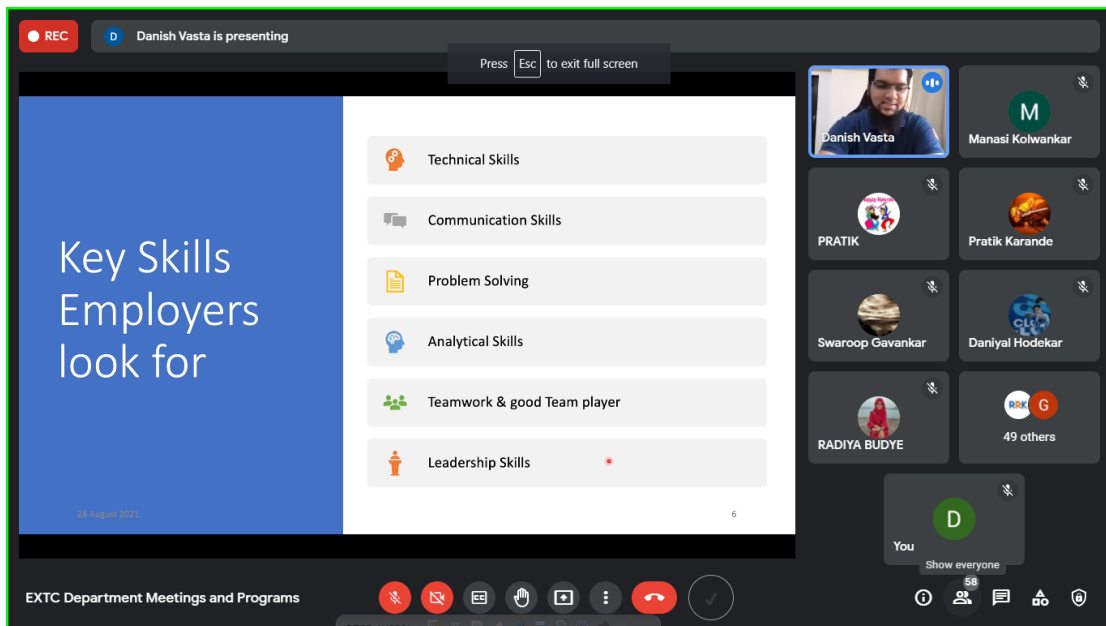
It was attended by 50+ students of T.E. and B.E. EXTC along with the faculty members of the department. The session helped students to know about different opportunities in the embedded field and basic skill sets in electronics, telecommunication and Programming.

Mr Danish Vasta highlighted the role of testing engineers in the industry and briefed about key skills employers looks for in the field of Embedded Engineering. He also explained how final year projects should be selected and executed along with the importance of self-learning while doing the projects.

The session was superintended by the Head of Electronics and Telecommunication Engineering Department, Dr S. V. Chougule along with Prof. Mansi Kolwankar as the Departmental coordinator. The participants interacted with Mr Vasta and also showed interest in attending alumni sessions in future.



**A glimpse of the presentation by Mr Danish Vasta (the Resource Person and Alumnus of EXTC)**



**Resource person briefing key skills required**

REC Danish Vasta is presenting

# Industry Specific

28 August 2021

- Automobile**
  - Advanced protocols – CAN / FSF / Flex Ray
  - Languages – Autostar / MISRA C
  - Debugging – JTAG / IDE
- Semiconductor**
  - Understanding on FPGA / Logic gates
  - Languages – Embedded C / C++ / Python / TCL / VHDL
  - Debugging – JTAG / USART
- IoT**
  - Advanced protocols – REST / MQTT / SoAP
  - Languages – Python / C++
  - Debugging – Postman / Insomnia
- Networking**
  - Languages – C / C++ / Shell Scripting
  - Debugging – Wireshark / Packet analyzers
  - Advanced protocols – ARP / DNS / Wireless security – MITM, phishing

EXTC Department Meetings and Programs

Participants: Danish Vasta, Manasi Kolwankar, PRATIK, Pratik Karande, Swaroop Gavankar, Daniyal Hodekar, RADIYA BUDYE, 48 others, You

**Mr Danish Vasta briefing the Industry-specific domains for EXTC students**