



Hope Foundation's
**Finolex Academy of Management & Technology,
Ratnagiri**
**Department of Electronics and Telecommunication
Engineering**

Training Program on VLSI and Embedded Systems

The Department of Electronics and Telecommunication Engineering organized a one-day “**Training program on VLSI and Embedded Systems**” on 15th February 2020 for Final year Diploma students.

The training was attended by a total of 35 students. Out of them, 33 participants were from Government Polytechnic, Ratnagiri and two participants from Rajendra Mane Polytechnic, Ambav. The training program provided the knowledge of simulation and synthesis using Quartus II software in the VLSI domain and Keil4 software for ARM in the Embedded domain. Along with this, a demonstration of B.E. projects was arranged to explore the real-time applications of technology.

The program started with the felicitation of Prof. **T. N. Hailnalkar**, Faculty, Government Polytechnic, Ratnagiri by **Dr. Kaushal K. Prasad**, Principal, FAMT. In his address, Dr. Kaushal Prasad explained the importance of higher education to diploma students for better career opportunities. **Dr. S. V. Chougule**, Head of Electronics and Telecommunication Engineering Department, delivered a welcome address. **Prof. V. V. Nimbalkar**, Assistant Professor, Electronics Engineering department presented an overview of the training program. Prof. S.S. Athalye guided the students about career planning in Engineering.



Dr. Kaushal Prasad felicitating Prof. T. N. Hailnalkar

The workshop was conducted in four sessions comprising of project demonstrations, laboratory visits, theory, and hands-on sessions. Selective projects of BE EXTC and Electronics Engineering department students were demonstrated. In the project demonstration, the students learned applications of technology and innovative ideas. They showed a deep interest in learning new things interaction with FAMT students. In the second

session, Prof. S. S. Goilkar, demonstrated Analog and Digital Communication experiments using communication trainer kits.



Project Demonstration and Laboratory visit

The post-lunch session started with the training program on VLSI. The career scope in VLSI domain and chip making was taught during theory and practical sessions. The students wrote a program of the combinational circuit using VHDL and implemented it on CPLD of family MAX3000A.

Prof. R.R. Kulkarni, E&Tc department worked as the resource person. Tejas Sarpodar and Reefat Solkar, T. E. EXTC students, worked as coordinators for the VLSI training session.



Resource Person:
Prof.R.R.Kulkarni
(EXTC dept.)

Training session on
VLSI

Resource person:
Prof. V.V.Nimbalkar
(ETRX dept.)

Training session on
Embedded Systems



Resource persons interacting with participants

The training program on Embedded systems was conducted by **Prof. V. V. Nimbalkar**, Electronics Department. She explained the processors of smartphones and computers, SOC, the importance of learning ARM, ARM architecture, and Embedded C language. During the practical session, the students wrote an embedded C program for tasks such as LED blinking on Keil4 software and implemented it on the LPC2148 controller. Narendra Warang, Shivam Todankar, Abhishek Lingayat, Ruturaj Joshi and Suyog Gokhale, students of TE E&TC, were the coordinators for Embedded System's training session.

The Training program was supervised by the Head of Electronics and Telecommunication Engineering Department, Dr. S. V. Chougule. She thanked the participants and offered them the best wishes. The program was ended with a certificate distribution to all participants. **Mr. S. R. Pardule** offered very active support for the smooth conduct of the workshop.



Certificate Distribution